Bit Vectoring Data Flow Problems

The four data flow problems we have just reviewed all fit within a single framework.

Their solution values are Booleans (bits).
The meet operation is And or OR.
The transfer function is of the general form

$\text{Out}(b) = (\text{In}(b) - \text{Kill}_b) \cup \text{Gen}_b$

or

$\text{In}(b) = (\text{Out}(b) - \text{Kill}_b) \cup \text{Gen}_b$

where $\text{Kill}_b$ is true if a value is “killed” within $b$ and $\text{Gen}_b$ is true if a value is “generated” within $b$.

In Boolean terms:

$\text{Out}(b) = (\text{In}(b) \land \neg \text{Kill}_b) \lor \text{Gen}_b$

or

$\text{In}(b) = (\text{Out}(b) \land \neg \text{Kill}_b) \lor \text{Gen}_b$

An advantage of a bit vectoring data flow problem is that we can do a series of data flow problem “in parallel” using a bit vector.

Hence using ordinary word-level ANDs, ORs, and NOTs, we can solve 32 (or 64) problems simultaneously.

Example

Do live variable analysis for $u$ and $v$, using a 2 bit vector:

\[
\begin{align*}
\text{Live} = 0,0 & \quad \text{Gen} = 0,0 \\
\text{Live} = 0,1 & \quad \text{Gen} = 0,0, \text{Kill} = 0,1 \\
\text{Live} = 1,1 & \quad \text{Gen} = 1,0, \text{Kill} = 0,0 \\
\text{Live} = 1,0 & \quad \text{Gen} = 0,0, \text{Kill} = 0,1 \\
\text{Live} = 1,1 & \quad \text{Gen} = 1,1, \text{Kill} = 0,1 \\
\text{print}(u,v) & \\
\end{align*}
\]

We expect no variable to be live at the start of $b_0$. (Why?)

Depth-First Spanning Trees

Sometimes we want to “cover” the nodes of a control flow graph with an acyclic structure.

This allows us to visit nodes once, without worrying about cycles or infinite loops.

Also, a careful visitation order can approximate forward control flow (very useful in solving forward data flow problems).

A Depth-First Spanning Tree (DFST) is a tree structure that covers the nodes of a control flow graph, with the start node serving as root of the DFST.
Building a DFST

We will visit CFG nodes in depth-first order, keeping arcs if the visited node hasn’t been reached before.

To create a DFST, T, from a CFG, G:
1. \( T \leftarrow \) empty tree
2. Mark all nodes in G as “unvisited.”
3. Call DF(start node)

DF (node) {
1. Mark node as visited.
2. For each successor, s, of node in G:
   If s is unvisited
   (a) Add node \( \rightarrow s \) to T
   (b) Call DF(s)

Example

\[ \text{Visit order is } A, B, C, D, E, G, H, I, J, F \]

The DFST is

Categorizing Arcs using a DFST

Arcs in a CFG can be categorized by examining the corresponding DFST.

An arc \( A \rightarrow B \) in a CFG is
(a) An Advancing Edge if B is a proper descendent of A in the DFST.
(b) A Retreating Edge if B is an ancestor of A in the DFST.
   (This includes the \( A \rightarrow A \) case.)
(c) A Cross Edge if B is neither a descendent nor an ancestor of A in the DFST.
**Depth-First Order**

Once we have a DFST, we can label nodes with a Depth-First Ordering (DFO).

Let $i =$ the number of nodes in a CFG (= the number of nodes in its DFST).

DFO(node) {
    For (each successor $s$ of node) do
        DFO(s);
    Mark node with $i$;
    $i$--;
}

**Application of Depth-First Ordering**

- Retreating edges (a necessary component of loops) are easy to identify:
  - $a \rightarrow b$ is a retreating edge if and only if $\text{dfo}(b) \leq \text{dfo}(a)$
- A depth-first ordering in an excellent visit order for solving forward data flow problems. We want to visit nodes in essentially topological order, so that all predecessors of a node are visited (and evaluated) before the node itself is.
**Dominators**

A CFG node $M$ dominates $N$ ($M \text{ dom } N$) if and only if all paths from the start node to $N$ must pass through $M$.

A node trivially dominates itself. Thus $(N \text{ dom } N)$ is always true.

A CFG node $M$ strictly dominates $N$ ($M \text{ sdom } N$) if and only if $(M \text{ dom } N)$ and $M \neq N$.

A node can’t strictly dominates itself. Thus $(N \text{ sdom } N)$ is never true.

**Immediate Dominators**

If a CFG node has more than one dominator (which is common), there is always a unique “closest” dominator called its immediate dominator.

$(M \text{ idom } N)$ if and only if

$(M \text{ sdom } N)$ and

$(P \text{ sdom } N) \Rightarrow (P \text{ dom } M)$

To see that an immediate dominator always exists (except for the start node) and is unique, assume that node $N$ is strictly dominated by $M_1$, $M_2$, ..., $M_p$, $P \geq 2$.

By definition, $M_1$, ..., $M_p$ must appear on all paths to $N$, including acyclic paths.

**A CFG node may have many dominators.**

![Diagram](image)

Node $F$ is dominated by $F$, $E$, $D$ and $A$.

Look at the relative ordering among $M_1$ to $M_p$ on some arbitrary acyclic path from the start node to $N$.

Assume that $M_i$ is “last” on that path (and hence “nearest” to $N$).

If, on some other acyclic path, $M_j \neq M_i$ is last, then we can shorten this second path by going directly from $M_i$ to $N$ without touching any more of the $M_1$ to $M_p$ nodes.

But, this totally removes $M_j$ from the path, contradicting the assumption that $(M_j \text{ sdom } N)$. 
**Dominator Trees**

Using immediate dominators, we can create a dominator tree in which \(A \rightarrow B\) in the dominator tree if and only if (A idom B).

**A Dominator Tree is a compact and convenient representation of both the dom and idom relations.**

A node in a Dominator Tree dominates all its descendents in the tree, and immediately dominates all its children.

**Computing Dominators**

Dominators can be computed as a Set-valued Forward Data Flow Problem.

If a node \(N\) dominates all of node \(M\)'s predecessors, then \(N\) appears on all paths to \(M\). Hence (\(N\) dom \(M\)).

Similarly, if \(M\) doesn’t dominate all of \(M\)'s predecessors, then there is a path to \(M\) that doesn’t include \(M\). Hence \(\neg(N\ dom\ M)\).

These observations give us a “data flow equation” for dominator sets:

\[
\text{dom}(N) = \{N\} \cup \bigcap_{M \in \text{Pred}(N)} \text{dom}(M)
\]
The analysis domain is the lattice of all subsets of nodes. Top is the set of all nodes; bottom is the empty set. The ordering relation is subset.

The meet operation is intersection.

The Initial Condition is that
\[ \text{DomIn}(b_0) = \phi \]
\[ \text{DomIn}(b) = \bigcap_{c \in \text{Pred}(b)} \text{DomOut}(c) \]
\[ \text{DomOut}(b) = \text{DomIn}(b) \cup \{b\} \]

Loops Require Care

Loops in the Control Flow Graph induce circularities in the Data Flow equations for Dominators. In

\[
\begin{array}{c}
A \\
B \\
C
\end{array}
\]

we have the rule
\[
\text{dom}(B) = \text{DomOut}(B) = \\
\text{DomIn}(B) = \{B\} = \\
\{B\} \cup (\text{DomOut}(B) \cap \text{DomOut}(A))
\]

If we choose \( \text{DomOut}(B) = \phi \) initially, we get \( \text{DomOut}(B) = \{B\} \cup (\phi \cap \text{DomOut}(A)) = \{B\} \)

which is wrong.

Instead, we should use the Universal Set (of all nodes) which is the identity for \( \cap \).

Then we get
\[
\text{DomOut}(B) = \\
\{B\} \cup (\text{all nodes} \cap \text{DomOut}(A)) = \\
\{B\} \cup \text{DomOut}(A)
\]

which is correct.

A Worklist Algorithm for Dominators

The data flow equations we have developed for dominators can be evaluated using a simple Worklist Algorithm.

Initially, each node’s dominator set is set to the set of all nodes. We add the start node to our worklist.

For each node on the worklist, we reevaluate its dominator set. If the set changes, the updated dominator set is used, and all the node’s successors are added to the worklist (so that the updated dominator set can be propagated).
The algorithm terminates when the worklist becomes empty, indicating that a stable solution has been found.

**Compute Dominators()**

For (each \( n \in \text{NodeSet} \))

\[ \text{Dom}(n) = \text{NodeSet} \]

WorkList = \{StartNode\}

While (WorkList \( \neq \phi \)) {

Remove any node \( Y \) from WorkList

New = \( \{Y\} \cup \cap \text{Dom}(X) \)

\( X \in \text{Pred}(Y) \)

If New \( \neq \text{Dom}(Y) \) {

\[ \text{Dom}(Y) = \text{New} \]

For (each \( Z \in \text{Succ}(Y) \))

WorkList = WorkList U \{Z\}

}\}\}

Example

![Control Flow Graph]

Initially the WorkList = \{Start\}.

Be careful when \( \text{Pred}(\text{Node}) = \phi \).

**Postdominance**

A block \( Z \) postdominates a block \( Y \) (\( Z \text{pdom} Y \)) if and only if all paths from \( Y \) to an exit block must pass through \( Z \). Notions of immediate postdominance and a postdominator tree carry over.

Note that if a CFG has a single exit node, then postdominance is equivalent to dominance if flow is reversed (going from the exit node to the start node).