Errata

cp and cpi

- cpi instruction has been removed.
- The cp instruction sets the flags as following:
  - N is set if op1 > op2
  - Z is set if op1 == op2
  - C is set if op1 < op2
- Values for op1 and op2 are interpreted as 2’s complement
- All branches that have been discussed in class, e.g. breq, brsh, brlo, and brne all operate on these flags intuitively.
- Because of this, it is highly recommended to use a cp instruction before a branch instruction.

Assembler Directives

- Assembler directives now use a label, not a memory address in their declaration.
- The values are stored starting at the label.
- Example:
  - .byte(fib) 0,1,1,2,3,5
  - ldi r26, lo8(fib)
  - ldi r27, hi8(fib)
  - ld r25, X; Loaded r25 with the first number in the array