

# CS354: Machine Organization and Programming

Lecture 16  
Friday the October 09<sup>th</sup> 2015

Section 2  
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## Class Announcements

1. Midterm 1 grades should be available by Monday next week.
2. Programming Assignment 1 will also be likely graded before early next week.

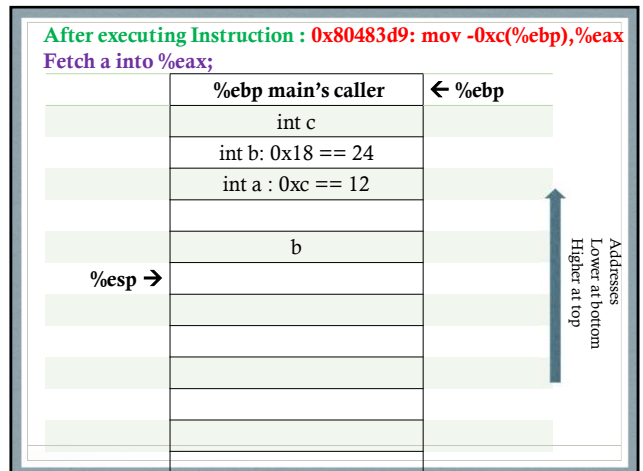
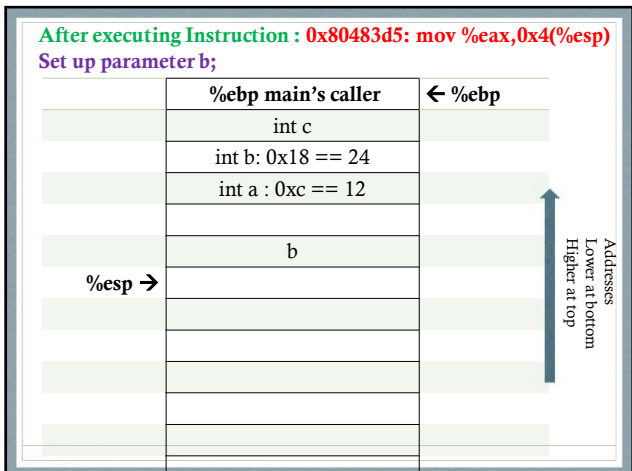
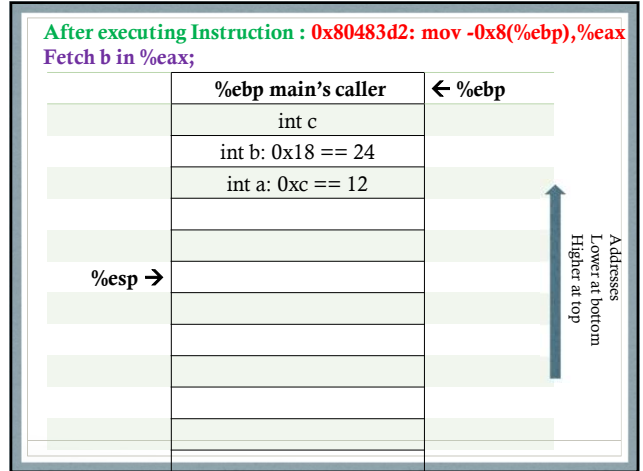
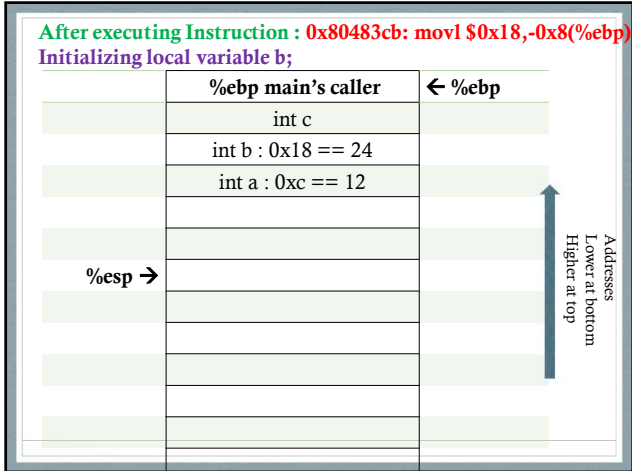
## Lecture Overview

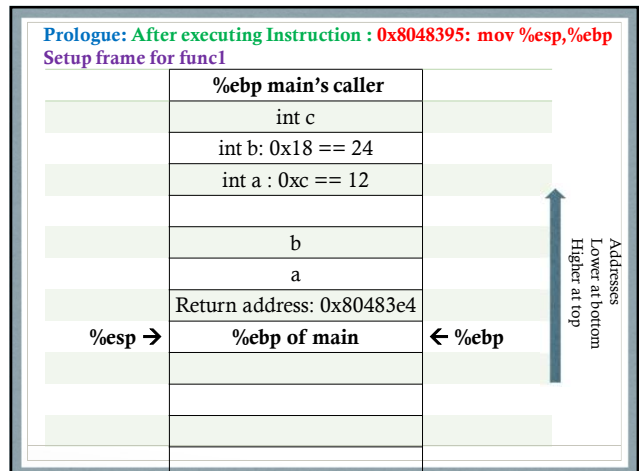
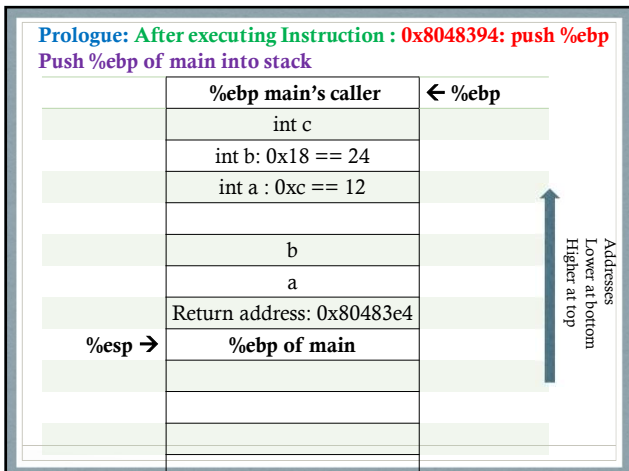
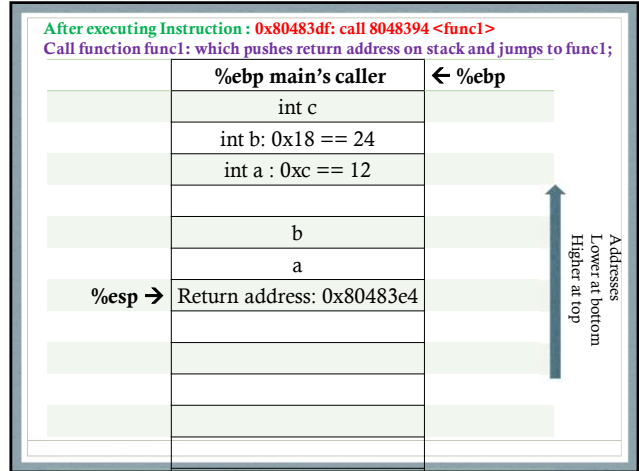
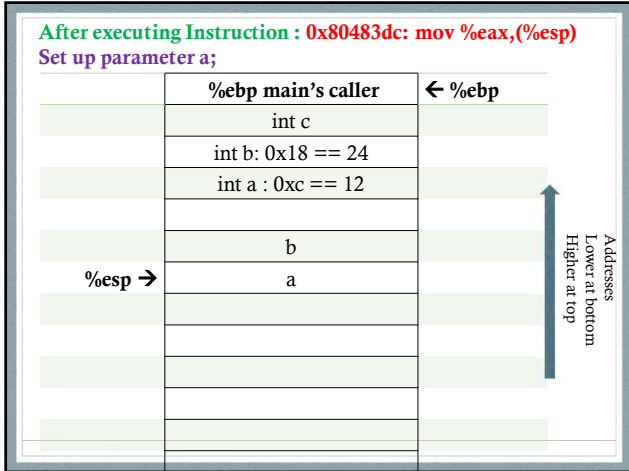
1. Demo of function calls using gdb along with slides that show how the stack changes during a simple function call.
2. Calling Conventions
3. Overview of Function calls

## Demo

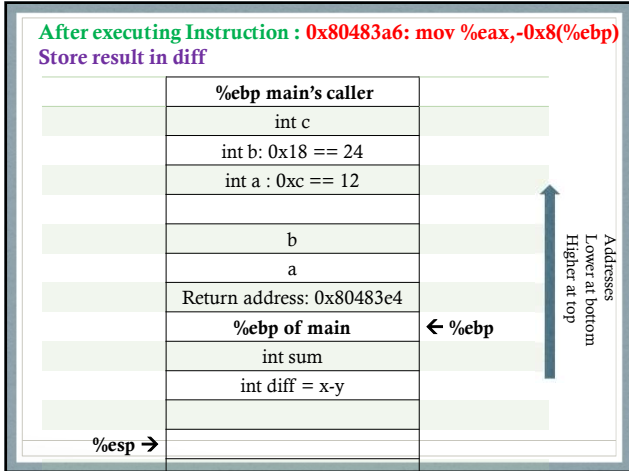
1. The following slides step through the assembly instructions for the program `simplefunctions1.c` from Lecture 16 and show how the stack changes.
2. Keep the files `simplefunctions1.c` and `simplefunctions1.objdump` open while going over the following slides that show the stack layout.





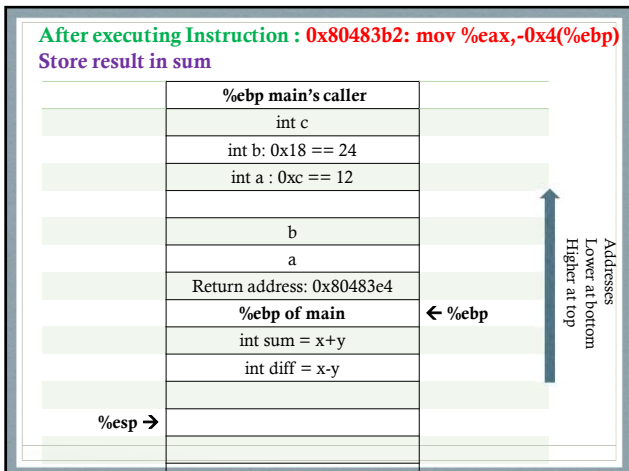






**After executing Instructions :**  
**0x80483a9: mov 0xc(%ebp),%eax**  
**0x80483ac: mov 0x8(%ebp),%edx**  
**0x80483af: lea (%edx,%eax,1),%eax**

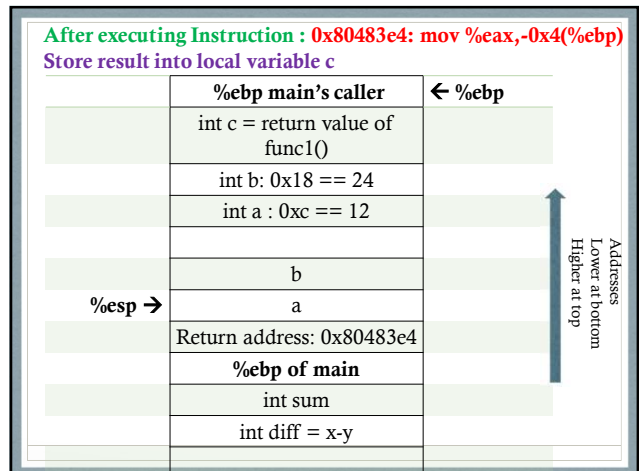
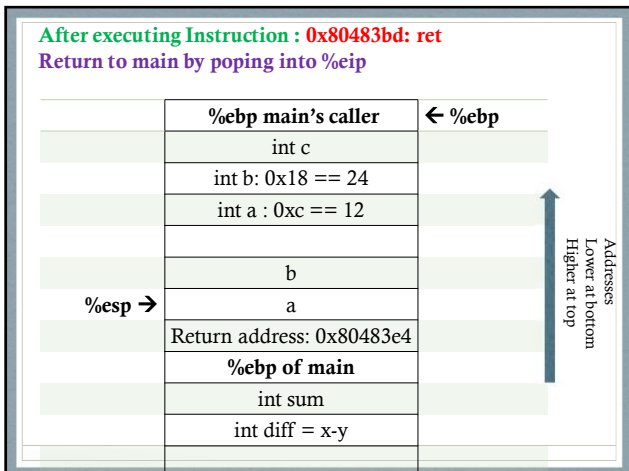
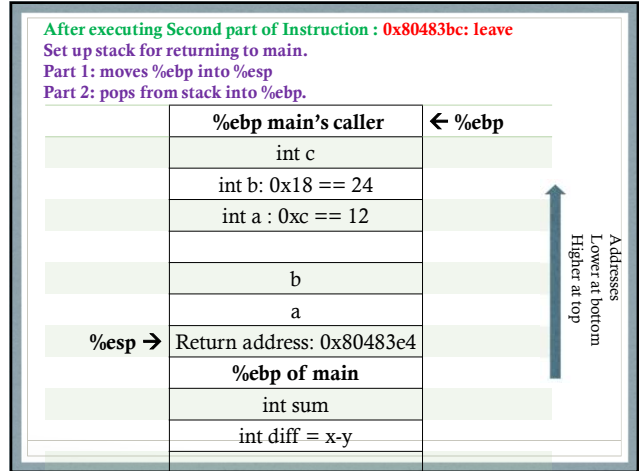
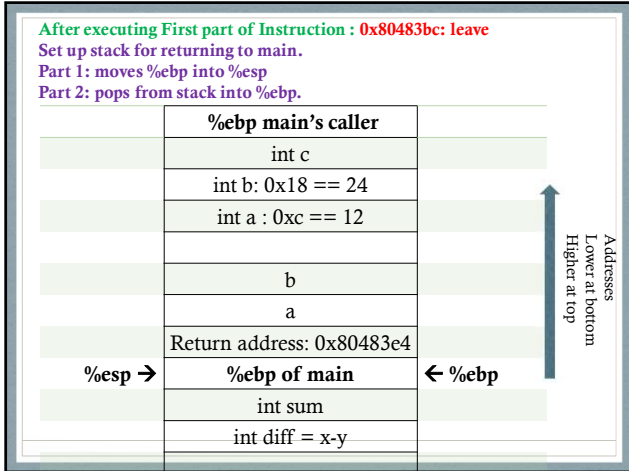
**These instruction fetch parameters x, y into temporary registers, calculate x+y into register %eax**

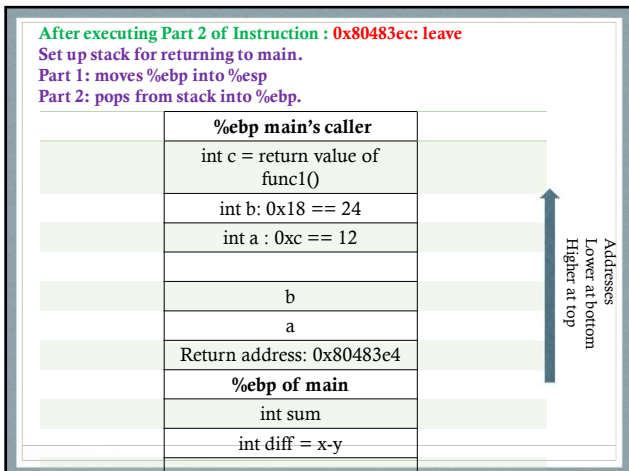
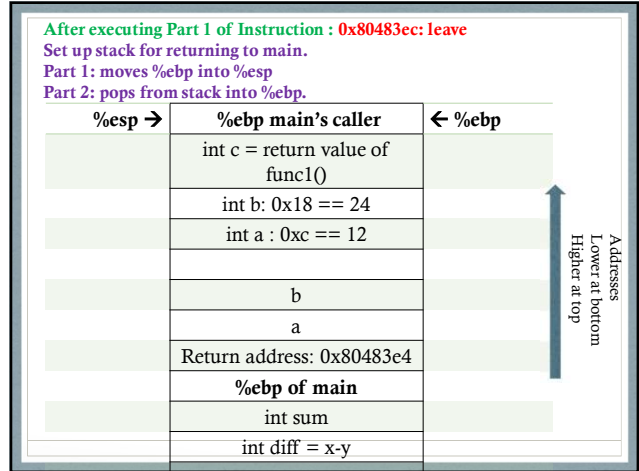
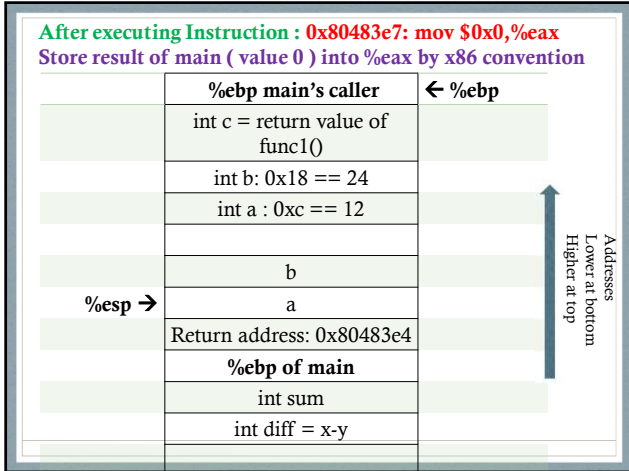


**After executing Instructions :**  
**0x80483b5: mov -0x4(%ebp),%eax**  
**0x80483b8: imul -0x8(%ebp),%eax**

**These instructions fetch sum into %eax, and then calculate product of sum and diff into register %eax**

**Since by x86 conventions, the result of a function is left in %eax, we do not need to anything further.**





### Register Saving x86 conventions

What if caller does

```
a: add  %edx, %ecx
   call b
   add  %ecx, %eax
```

Terminology: %ecx is live across the call to b



We need a *convention* that the compiler can implement for

1. responsibility for saving/restoring register contents
2. location of saved register contents

### IA 32 convention

#### caller save

`%eax` `%edx` `%ecx`

#### callee save

`%ebx` `%esi` `%edi`

Which is `%ebp` ?

With the convention, a's code becomes

```
a:  addl  %edx, %ecx
     pushl %ecx
     call b
     popl  %ecx
     add  %ecx, %eax
```

using caller save register

### How a caller/parent uses a callee save register

```
parent: pushl %ebp
        movl %esp, %ebp
        subl $16, %esp    includes space for callee save regs
        movl %ebx, -4(%ebp)  save callee save registers
        movl %edi, -8(%ebp)
        ( use %ebx and %edi )
        call child
        ( use %ebx and %edi some more )
        movl -4(%ebp), %ebx  restore callee save registers
        movl -8(%ebp), %edi
        leave
        ret
```

Example Program on  
Register Calling  
Conventions