

Virtual Memory

- OS must manage physical memory

One single physical resource

- all programs want to use same addresses

- stack $0xffff...$

- code $0x80...$

- portability

- What if physical memory is smaller than expected?

- make memory seem larger than it is (swapping)

★ - protection

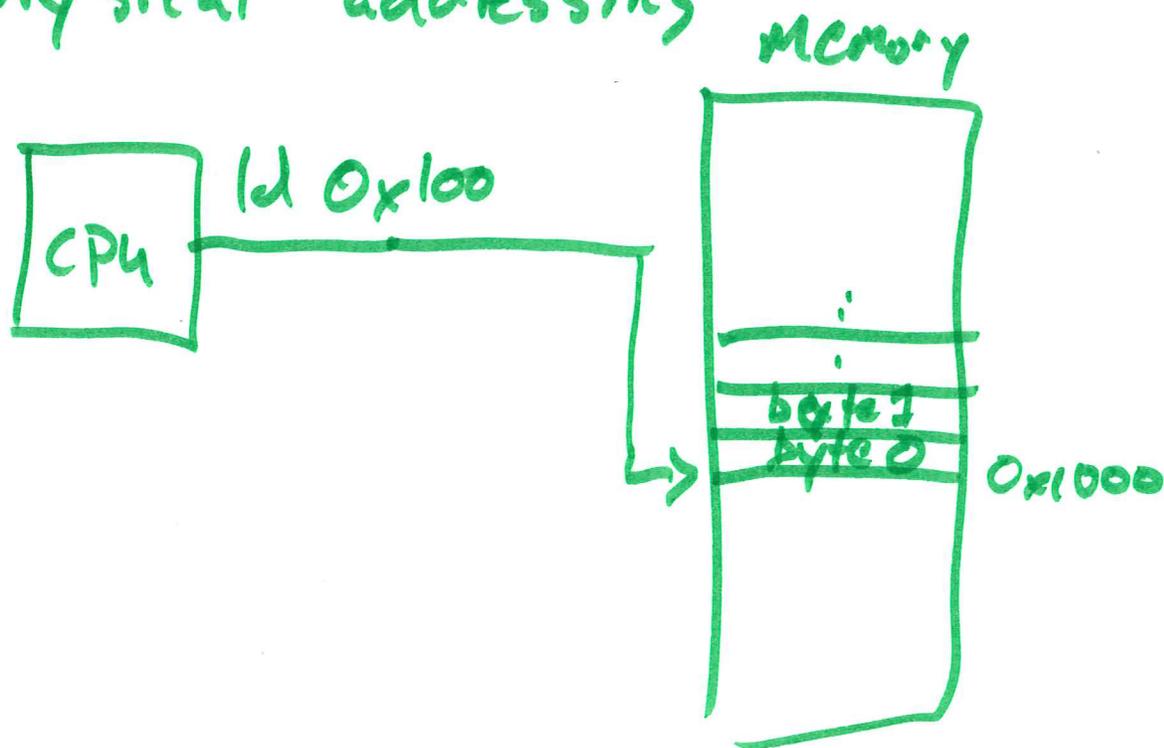
- isolates your data

- protects other's data

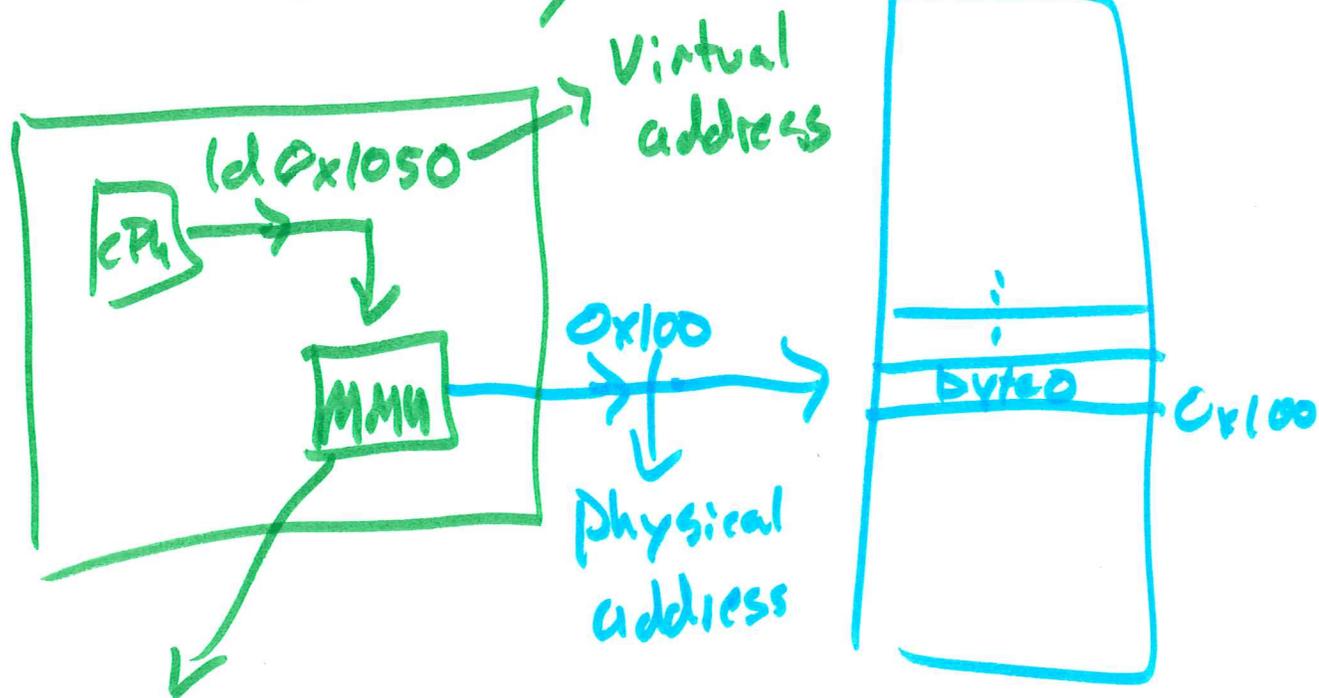
- Sharing between processes

- 1) how VM works
 - 2) how VM is actually implemented
 - 3) speeding up VM (caching)
 - 4) memory management user-level (malloc)
 - 5) code + memory (load + link)
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Physical addressing



Virtual addressing



MMU - Memory Management Unit

address space: Set of addresses (usually linear) 64-bits today

Virtual address space $\{0 \dots N-1\}$ 2^n
 $n \rightarrow$ # of bits

Physical address space $\{0 \dots M-1\}$

Program's address space
 \rightarrow memory the program thinks it can access
 virtual address space

$\downarrow \downarrow 2^m$
 defined by H/W
 40-48 bits

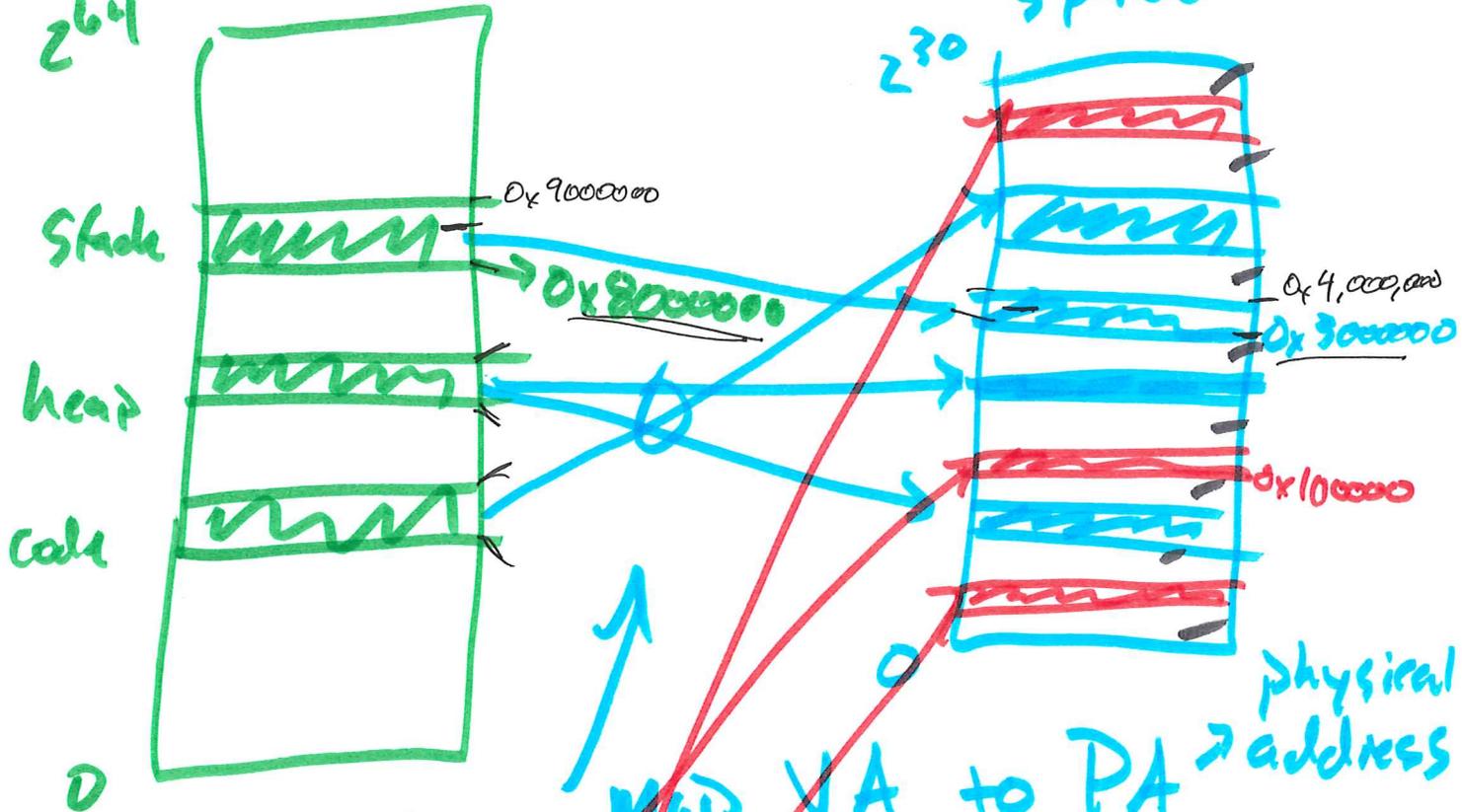
int a = 12;

&a → what's possible here?

P1

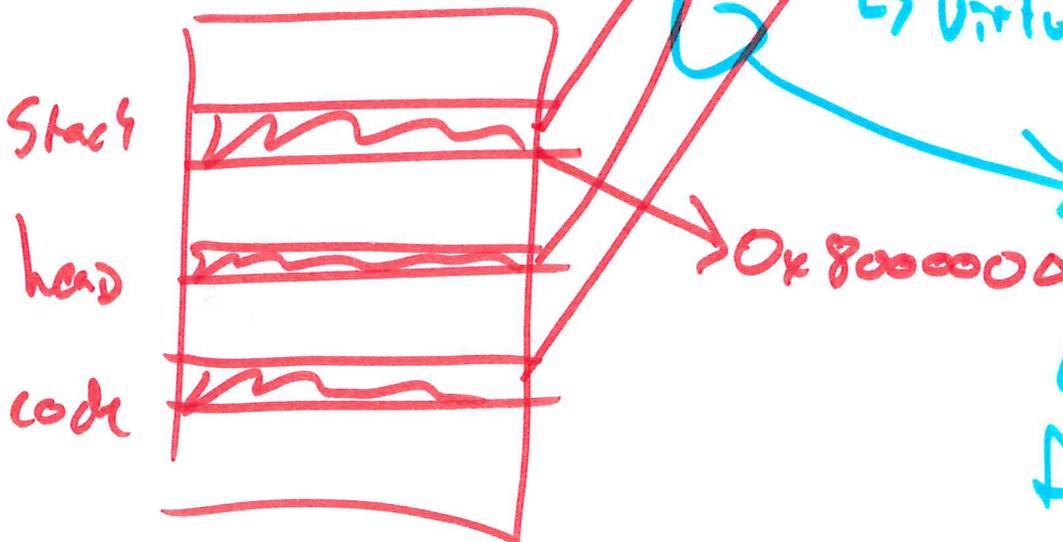
Virtual address space
 2^{64}

Physical address space
 2^{30}



Map VA to PA
↳ virtual address

P2 virt. addr. space



Translation

↳ Conversion from virt. to phys

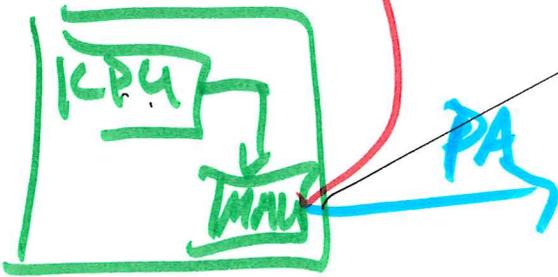
Translation table

	Base	translation	size
P1	<u>0x8000000</u>	<u>→ 0x3000000</u>	<u>1,000,000</u>
P2	<u>0x8000000</u>	<u>→ 0x10000000</u>	<u>1,500,000</u>

↑ Segmentation → base bounds (size) translation

P1

0x1000000 → 0x3000000
 0x8000000 → 0x0000000



P2
 0x8000000 → 0x10000000

Segmentation leads to fragmentation
 Solution? →