# **CS/ECE 252: INTRODUCTION TO COMPUTER ENGINEERING**

## **UNIVERSITY OF WISCONSIN—MADISON**

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Midterm Examination 1

In Class (50 minutes)

Friday, February 10, 2017

Weight: 17.5%

#### NO: BOOK(S), NOTE(S), OR CALCULATORS OF ANY SORT.

The exam has seven pages. **Circle your final answers**. Plan your time carefully since some problems are longer than others. You **must turn all pages**.

LAST NAME: \_\_\_\_\_

FIRST NAME: \_\_\_\_\_

ID#

Problem	Maximum Points	<b>Points Earned</b>
1	1	
2	4	
3	2	
4	3	
5	4	
6	3	
7	2	
8	5	
9	2	
10	3	
11	3	
Total	32	

## Problem 1 (1 point)

Which of the below is **not** a property of an algorithm? (Choose one option.)

- a) An algorithm must be efficient.
- b) An algorithm must successfully terminate.
- c) Each step of an algorithm must be precisely defined.
- d) Each step of algorithm must be able to be carried out by a computer.

### Problem 2 (4 points)

For each of the following statements, indicate whether it is true or false. If it is false, explain why it is incorrect.

- a) The decimal number 5 can be represented with a 3-bit 2's complement number.
- b) Statements in assembly language can have multiple interpretations.

c) An assembler converts an assembly language to the corresponding ISA.

d) Microarchitecture defines the set of instructions that a computer can carry out.

#### Problem 3 (2 points)

A teacher grades each question on an exam on a scale of -11 to +11 (inclusive, and only integers).

a) What is the minimum number of bits needed to represent the score for each question in 2's complement representation? Show your work for full credit.

b) Assuming there are 3 questions in the exam, and each question is graded the same way mentioned above, what is the minimum number of bits needed to represent the **total** score for the exam in 2's complement representation? Explain your answer.

## Problem 4 (3 points)

4. Perform the calculations below on the following **two's complement** numbers. Show your work for full credit.



+ 01010110

c) Did an overflow error occur in (a) or (b)? State how you know.

## Problem 5 (4 points)

Fill in the table for the following equations given the starting values A, B, and C.

Q1 = (A AND B) OR NOT CQ2 = B OR NOT (C AND B)

Α	В	С	Q1	Q2
1010	1000	1001		
0111	1011	1001		

#### Problem 6 (3 points)

Perform the conversions below. Show your work for full credit.

- a) Convert the decimal number -127 to 8-bit 2's complement.
- b) Convert the 8-bit 2's complement 10101111 to decimal.

#### Problem 7 (2 points)

You have a new 8-bit fixed point binary notation. The bits will be divided as follows: 1 sign bit, 3 bits for the integer part, and 4 bits for the fractional part. xB2 is a a bit pattern for a number in our new format. What is the **decimal** equivalent? Explain your answer.

(x stands for Hexadecimal representation. Example: xA = 1010 in binary)

## Problem 8 (5 points)

You are given a 8-bit binary number A = 11111111.

a) Write the decimal equivalent of A, assuming A is represented in 2's complement form.

- b) Write the decimal equivalent of A, assuming A is represented in 1's complement form.
- c) What is the smallest decimal number that can be represented with an 8-bit **2's complement** number?
- d) What is the smallest decimal number that can be represented with an **8-bit 1's complement** number?
- e) How many unique numbers can be represented using 8-bit **1's complement**? Show your answer in **decimal** (exponent answers are allowed, ex: 2<sup>5</sup>).

### Problem 9 (2 points)

Sign (1 bit)		Exponent (8 bits)	Fraction (23 bits)
	sign	Exponent-127	

where the value  $N = (-1)^{Sign} \times 1$ . Fraction  $\times 2^{Exponent-12/2}$ .

#### Problem 10 (3 points)

a) Add the following hex values together. Your answer should be in **16-bit binary.** Show your work for full credit.

xB3D3 + x4BC8

b) Interpret your answer from (a) as **16-bit two's complement.** What is its **decimal** equivalent? Show your work for full credit.

### Problem 11 (3 points)

You are given a 8-bit binary number **A** represented in 2's complement form. Your aim is to turn off the rightmost bit of **A** which is **ON** (i.e. equal to 1). For example if A= 0111 0110, after turning off the rightmost **ON** bit of **A**, we get **B** = 0111 0100. Answer the following questions:

- a) Calculate the value of A-1 (using the value of A=0111 0110). Express your answer in 8-bit 2's complement binary representation.
- b) How would you obtain **B** from **A** and **A-1** using exactly one of the following logical operations: AND, OR, NOT, XOR ?