NO BOOK(S), NOTE(S), CALCULATORS OR ELECTRONIC DEVICES OF ANY SORT.
The exam has eleven pages. You must turn in the pages 1-9. Circle your final answers. Plan your time carefully since some problems are longer than others. Use the blank sides of the exam for scratch work.
<table>
<thead>
<tr>
<th>Problem</th>
<th>Maximum Points</th>
<th>Points Earned</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
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<td>7</td>
<td>4</td>
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<tr>
<td>Total</td>
<td>32</td>
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</tbody>
</table>
1. The following LC-3 assembly code contains assembly syntax errors. Identify and fix at least 4 errors. (4 points)

```asm
.ORIG x3000
ADD R3, R3, ONE
LD R5, STRING
LOOP ADD R1, R1, #32
BRz LOOP
MAIN LD R4, MAIN
    SUB R3, R3, #1
    ST R3, STRING
LOOP HALT
ONE .FILL #1
    .BLKW 4
    .BLKW 3
STRING .STRINGZ "ABC"
.END
.ORIG x3000
ADD R3, R3, ONE
    LD R5, STRING
LOOP ADD R1, R1, #32
BRz LOOP
MAIN LD R4, MAIN
    SUB R3, R3, #1
    ST R3, STRING
LOOP HALT
ONE .FILL #1
    .BLKW 4
    .BLKW 3
STRING .STRINGZ "ABC"
.END
```
2. a) Fill in the symbol table for following LC-3 assembly code. You may not need to fill all rows. (5 points)

```
.ORIG x3000
AND R3, R3, #0
LD R4, VAL1
LOOP BRz EXIT
  JSR INC
  JSR DEC
EXIT    ST R3, ANS
         PUTS
         HALT
INC     ADD R3, R3, #1
        RET
DEC     ADD R4, R4, #-1
        RET ; Storage area for variables below:
ANS     .BLKW #5
VAL0    .STRINGZ "252"
VAL1    .STRINGZ "CS"
.END
```

<table>
<thead>
<tr>
<th>SYMBOL</th>
<th>Value (in hex)</th>
</tr>
</thead>
<tbody>
<tr>
<td>LOOP</td>
<td>0x3002</td>
</tr>
<tr>
<td>EXIT</td>
<td>0x3005</td>
</tr>
<tr>
<td>INC</td>
<td>0x3008</td>
</tr>
<tr>
<td>DEC</td>
<td>0x300A</td>
</tr>
<tr>
<td>ANS</td>
<td>0x300C</td>
</tr>
<tr>
<td>VAL0</td>
<td>0x3011</td>
</tr>
<tr>
<td>VAL1</td>
<td>0x3015</td>
</tr>
</tbody>
</table>

b) Convert the instruction stored at memory location 0x3006 into binary.

0xF022 (TRAP x22)
3. An LC-3 assembly code is given below: (5 points)

```
.ORIG x3000
AND R0, R0, #0
LD R0, DATA
ADD R0, R0, #11
PRINT1 OUT
   AND R0, R0, #0
   ADD R0, R7, R0
PRINT2 OUT
   HALT
DATA .FILL 0xFFFF
```

a. What is the output (in hex) after the OUT statement at the symbol PRINT1 finishes execution?

0x000A

b. What is the output (in hex) after the OUT statement at the symbol PRINT2 finishes execution? Explain your answer.

0x3004

c. Complete the following incomplete code snippet that uses the memory-mapped LC-3 registers KBSR and KBDR to take input from the keyboard instead of the GETC instruction. Your code should store the value entered from keyboard in register R3. (Assume KBSR is mapped to address xFE00 and KBDR is mapped to address xFE02.)

```
ECHO: ______,___, KBSR
    _____, R3, KBDR
KBSR .FILL xFE00
KBDR .FILL xFE02

ECHO: LDI, R0, KBSR
   BRnp ECHO
   LDI R2, KBDR

KBSR .FILL xFE00
KBDR .FILL xFE02
```
4. Short answer questions (5 points)

a. Briefly describe what happens in the **linking** phase of an assembly program.

*Linking is resolving symbols between independent object files.*

b. How are the Display Data Register (DDR) and Display Status Register (DSR) used when TRAP x21 (OUT) is called?

*When the monitor is ready to display another character, DSR[15] is set to 1. DSR[7:0] is displayed and DSR[15] is set to 1. Any other data written to DDR is ignored while DSR[15] is zero.*

c. What is a **service routine** in LC-3? Give an example.

*A service routine is a function that performs a specific operation (optionally as part of the operating system). In LC-3, traps are service routines. Any trap will work here as an example.*

d. **Briefly** describe the difference between synchronous and asynchronous I/O.

*Synchronous I/O events occur at fixed, predictable rates. CPU reads every X seconds. Asynchronous I/O is unpredictable. Can use flag to achieve I/O. Example is keyboard input.*
5. The following LC-3 assembly code implements a **subroutine**. After taking input, it outputs a lowercase letter (a-z) if the input was uppercase (A-Z), and a “N” otherwise. (5 points)

```assembly
SUBR GETC
    ADD R5, R0, 0
    NOT R5, R5
    ADD R5, R5, 1
    LD R1, LBOUND
    LD R2, UBOUND
    ADD R3, R5, R2
    BRn PNOT
    ADD R3, R5, R1
    BRp PNOT
    LD R3, DIFF
    ADD R0, R0, R3
    OUT
    BRnzp FINISH

PNOT LD R0, N
    OUT
    FINISH RET
```

UBOUND .FILL x5A ;ASCII value of "Z"
LBOUND .FILL x41 ;ASCII value of "A"
N .FILL x4E ;ASCII value of "N"
DIFF .FILL x20

a. Is this code able to successfully return? Explain why or why not.

*No. R7 is overwritten by GETC and OUT.*

b. Add lines to the code above to make this subroutine **callee-saved**. You should not modify any existing lines. **Clearly indicate which lines you have added and where.**

**Before GETC:**
ST R0, SAVE0
ST R1, SAVE1
ST R2, SAVE2
ST R3, SAVE3
ST R5, SAVE5
ST R7, SAVE7

Before FINISH:
LD R0, SAVE0
LD R1, SAVE1
LD R2, SAVE2
LD R3, SAVE3
LD R5, SAVE5
LD R7, SAVE7

After DIFF
SAVE0 .FILL x0
SAVE1 .FILL x0
SAVE2 .FILL x0
SAVE3 .FILL x0
SAVE5 .FILL x0
SAVE7 .FILL x0
6. Examine the code below. You may assume that at the start of the program, all registers are set to 0. (4 points)

```assembly
.ORIG x3000
GETC
AND R3, R3, 0
ADD R3, R3, R0

LD R1, ONE
NOT R1, R1
ADD R1, R1, 1

PRINT LD R0, C
OUT
LD R0, A
OUT
LD R0, T
OUT

ADD R0, R3, R1
BRz FINISH

LD R0, S
OUT
FINISH HALT
```

C .FILL x63 ; ASCII 'c'
A .FILL x61 ; ASCII 'a'
T .FILL x74 ; ASCII 't'
S .FILL x73 ; ASCII 's'
ONE .FILL x31 ; ASCII '1'

a. If the input is the decimal number "3", what is output on the screen at the end of the program?

cats

b. OUT only prints out one character at a time. Instead of printing individually, we decide to replace the code stored at memory locations x3006 to x300B with the following, much shorter block of code:

```assembly
PRINT LD R0, C
PUTS
```

Will this output the same result as a)? Why or why not? **You must explain your answer for credit.**

No. It'll output a bunch of garbage (whatever is stored at memory location x63 until it hits a null terminator). This is because Puts uses the register value as an address, not as a value.
7. Multiple choice questions. Circle **one** answer for each question. (4 points)

(i) Which of the following can **not** be used multiple times in a single assembly program?
   a) .BLKW  
   b) .FILL  
   c) .ORIG  
   d) .STRINGZ

(ii) Assume that a LC-3 processor receives interrupts from 3 I/O devices (A, B and C) simultaneously. The priority levels for the interrupts are given below:
   A) PL0  
   B) PL7  
   C) PL6
Assuming that no other interrupts come in, which of the above interrupts is serviced **last**?
   a) A  
   b) B  
   c) C  
   d) Any selected at random

(iii) Our program begins at memory location x4000. We want to load the value x4020 into R3. Which LC-3 instruction can we use to accomplish this in a **single** line?
   a) LD  
   b) LEA  
   c) LDI  
   d) ST

(iv) Which register is used to store **input data** after IN is called?
   a) R7  
   b) R4  
   c) R1  
   d) R0
LC-3 Instruction Set

PC\(^{+}\): incremented PC. setcc(): set condition codes N, Z, and P. mem[A]: memory contents at address A.
SEXT(immediate): sign-extend immediate to 16 bits. ZEXT(immediate): zero-extend immediate to 16 bits.

<table>
<thead>
<tr>
<th>Instruction</th>
<th>Description</th>
</tr>
</thead>
<tbody>
<tr>
<td>ADD DR, SR1, SR2</td>
<td>Addition</td>
</tr>
<tr>
<td>ADD DR, SR1, imm5</td>
<td>Addition with Immediate</td>
</tr>
<tr>
<td>AND DR, SR1, SR2</td>
<td>Bit-wise AND</td>
</tr>
<tr>
<td>AND DR, SR1, imm5</td>
<td>Bit-wise AND with Immediate</td>
</tr>
<tr>
<td>BRx,label</td>
<td>Branch</td>
</tr>
<tr>
<td>JMP BaseR</td>
<td>Jump</td>
</tr>
<tr>
<td>JSR label</td>
<td>Jump to Subroutine</td>
</tr>
<tr>
<td>JSR BaseR</td>
<td>Jump to Subroutine in Register</td>
</tr>
<tr>
<td>LD DR, label</td>
<td>Load PC-Relative</td>
</tr>
<tr>
<td>LD DR, BaseR, offset6</td>
<td>Load Base+Offset</td>
</tr>
<tr>
<td>LD DR, BaseR, SEXT(offset6)</td>
<td>Load Base+SEXT(Offset6)</td>
</tr>
<tr>
<td>LEA DR, label</td>
<td>Load Effective Address</td>
</tr>
<tr>
<td>NOT DR, SR</td>
<td>Bit-wise Complement</td>
</tr>
<tr>
<td>RET</td>
<td>Return from Subroutine</td>
</tr>
<tr>
<td>RTI</td>
<td>Return from Interrupt</td>
</tr>
<tr>
<td>ST SR, label</td>
<td>Store PC-Relative</td>
</tr>
<tr>
<td>STI SR, label</td>
<td>Store Indirect</td>
</tr>
<tr>
<td>STR SR, BaseR, offset6</td>
<td>Store Base+Offset</td>
</tr>
<tr>
<td>TRAP</td>
<td>System Call</td>
</tr>
<tr>
<td>JSRR BaseR</td>
<td>Jump to Subroutine in Register</td>
</tr>
</tbody>
</table>

And more instructions...
### Assembler Directives

<table>
<thead>
<tr>
<th>Opcode</th>
<th>Operand</th>
</tr>
</thead>
<tbody>
<tr>
<td>.ORIG</td>
<td>address</td>
</tr>
<tr>
<td>.END</td>
<td></td>
</tr>
<tr>
<td>.BLKW</td>
<td>n</td>
</tr>
<tr>
<td>.FILL</td>
<td>n</td>
</tr>
<tr>
<td>.STRINGZ</td>
<td>n-character string</td>
</tr>
</tbody>
</table>

### Trap Codes

<table>
<thead>
<tr>
<th>Code</th>
<th>Equivalent</th>
</tr>
</thead>
<tbody>
<tr>
<td>HALT</td>
<td>TRAP x25</td>
</tr>
<tr>
<td>IN</td>
<td>TRAP x23</td>
</tr>
<tr>
<td>OUT</td>
<td>TRAP x21</td>
</tr>
<tr>
<td>GETC</td>
<td>TRAP x20</td>
</tr>
<tr>
<td>PUTS</td>
<td>TRAP x22</td>
</tr>
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</table>