Welcome back!

# VIRTUALIZATION: CPU

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# **ADMINISTRIVIA**

giklab reporitory

- Project I is out! Due on September 13<sup>th</sup> (Friday)
  - Check handin directory
  - Text cases -> Discussion
- Signup for Piazza <a href="https://piazza.com/wisc/fall2024/cs537">https://piazza.com/wisc/fall2024/cs537</a>
- Lecture notes at pages.cs.wisc.edu/~shivaram/cs537-fa24/
- Drop? Waitlist? Email enrollment@cs.wisc.edu and cc me

## AGENDA / OUTCOMES

#### **Abstraction**

What is a Process? What is its lifecycle?

#### Mechanism

How does process interact with the OS?

How does the OS switch between processes?

# **ABSTRACTION: PROCESS**

# PROGRAM VS PROCESS

```
) Chu.c
                                                          saved on disk
#include <stdio.h>
#include <stdlib.h>
#include "common.h"
                                                               Program
int main(int argc, char *argv[]) {
   char *str = argv[1];
   while (1) {
     printf("%s\n", str);
                                                                Process
     Spin(1);
   return 0;
```

#### WHAT IS A PROCESS?

Stream of executing instructions and their "context"

```
Instruction
Pointer
```

```
pushq
        %rbp
        %rsp, %rbp
movq
        $32, %rsp
suba
movl
        $0, -4(%rbp)
        %edi, -8(%rbp)
movl
        %rsi, -16(%rbp)
mova
        $2, -8(%rbp)
cmpl
je
        LBB0 2
```

Registers Memory addrs

Ly Mach

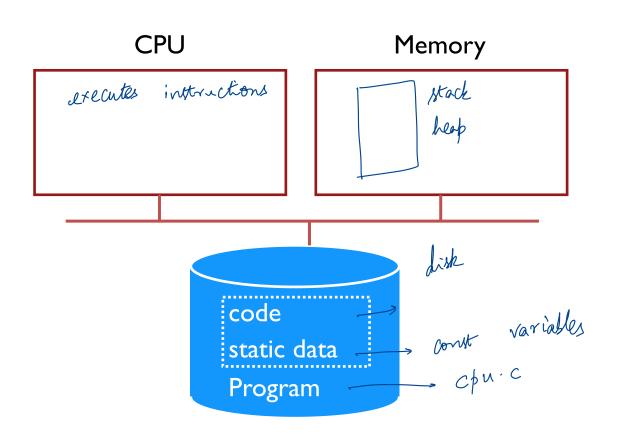
File descriptors

Lo opened

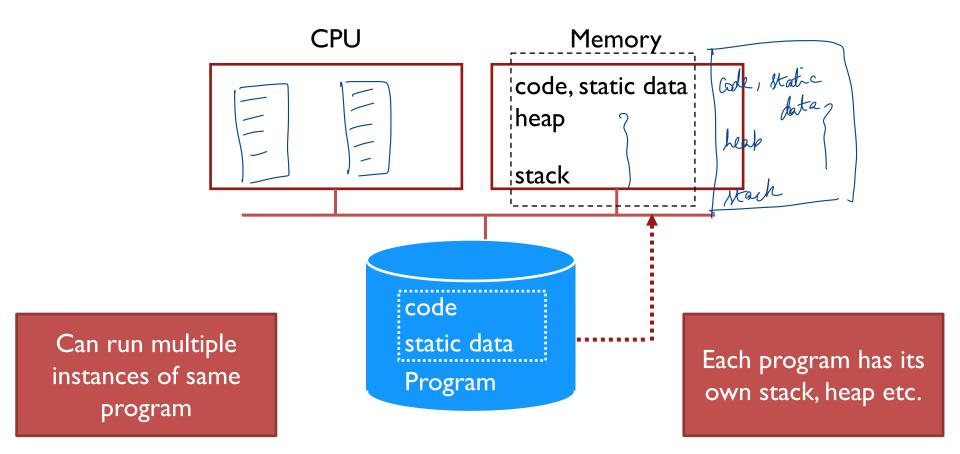
# PROCESS IN XV6

```
// Per-process state
struct proc {
  uint sz;
                              // Size of process memory (bytes)
  pde_t* pgdir;
                              // Page table
  char *kstack;
                              // Bottom of kernel stack for this proces
                                                                         struct context
                              // Process state
  enum procstate state;
                                                                           uint edi;
 int pid;
                              // Process ID
                                                                           uint esi;
 struct proc *parent;
                              // Parent process
                                                                           uint ebx;
  struct trapframe *tf; // Trap frame for current syscall
                                                                           uint ebp;
  struct context *context; // swtch() here to run process
                                                                           uint eip;
                              // If non-zero, sleeping on chan
  void *chan;
  int killed;
                              // If non-zero, have been killed
  struct file *ofile[NOFILE];
                             // Open files
  struct inode *cwd;
                             // Current directory
 char name[16];
                              // Process name (debugging)
```

## PROCESS CREATION



# PROCESS CREATION

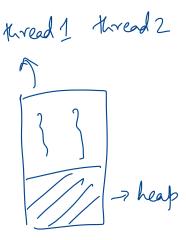


### PROCESS VS THREAD

Threads: "Lightweight process"

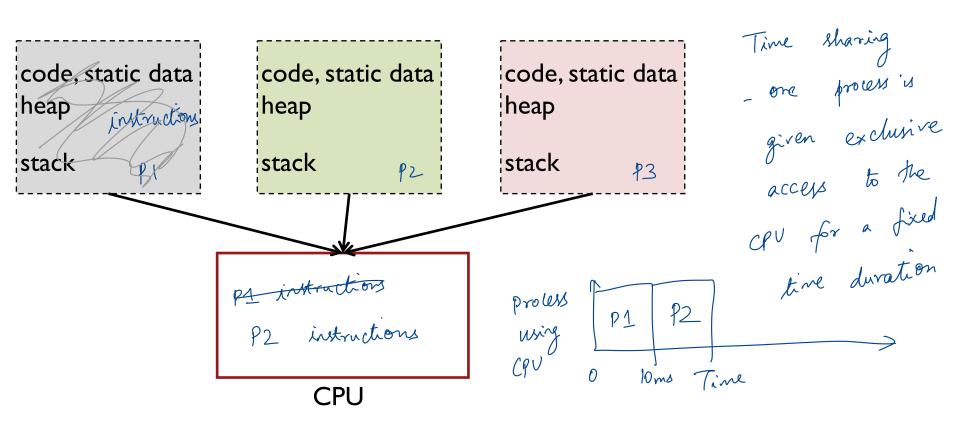
Execution streams that share an address space Can directly read / write memory

Can have multiple threads within a single process



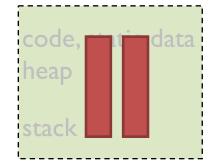
# SHARING THE CPU

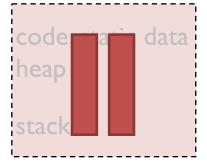
# **SHARING CPU**

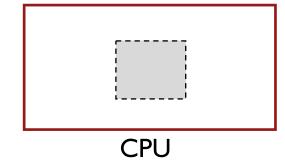


# TIME SHARING

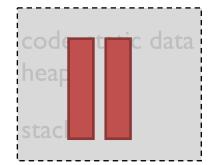
code, static data heap stack



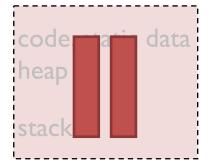


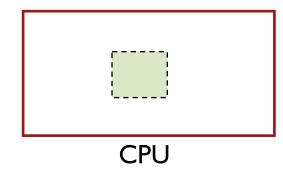


# TIME SHARING



code, static data heap stack





CPU

# WHAT TO DO WITH PROCESSES THAT ARE NOT RUNNING?

\$1,000

Process Pl

**OS Scheduler** 

Save context when process is paused

Restore context on resumption

STATE TRANSITIONS few milliseronds Process main () {

fopen ("board.txt") CPU is executing frutructions, created Descheduled, Ready + Mate Running Scheduled I/O: done I/O: initiate fopen () considered for scheduling **Blocked** why / when

# **ASIDE: OSTEP HOMEWORKS!**

- Optional homeworks corresponding to each chapter in book
- Little simulators to help you understand
- Can generate problems and solutions!

http://pages.cs.wisc.edu/~remzi/OSTEP/Homework/homework.html



# **PROCESS HW**

Run ./process-run.py — 1 2:100,2:0

QUIZ 1



https://tinyurl.com/cs537-fa24-quizla

≥ ./process-run.py -I 3:80,3:50

3 CPU slices per process

```
Time
                   PID: 0
                                     CPU
                           PID: 1
                                           I0s
                   RUN:io
                            READY
                  BLOCKED RUN:cpu
                  BLOCKED
                          RUN:io
                                                  Two Processes
                  BLOCKED BLOCKED ·
                  BLOCKED BLOCKED
                  BLOCKED BLOCKED
             RUN:io done BLOCKED
                       BLOCKED What happens at time 8?
               RUN: CPU
Each IO takes 5 time units,
```

## CPU SHARING

Policy goals

Virtualize CPU resource using processes

Reschedule process for fairness? efficiency?

Mechanism goals

-> low overhead Efficiency: Sharing should not add overhead

Control: OS should be able to intervene when required

# **EFFICIENT EXECUTION**

```
Simple answer !?: Direct Execution
```

Allow user process to run directly

Create process and transfer control to main() - program you are trying to run

#### Challenges

What if the process wants to do something restricted? Access disk? What if the process runs forever? Buggy? Malicious?

Solution: Limited Direct Execution (LDE)

## PROBLEM 1: RESTRICTED OPS

How can we ensure user process can't harm others?

Solution: privilege levels supported by hardware (bit of status)

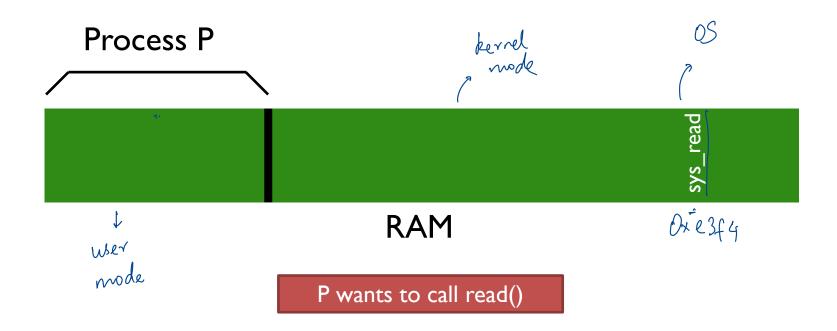
User processes run in user mode (restricted mode) —

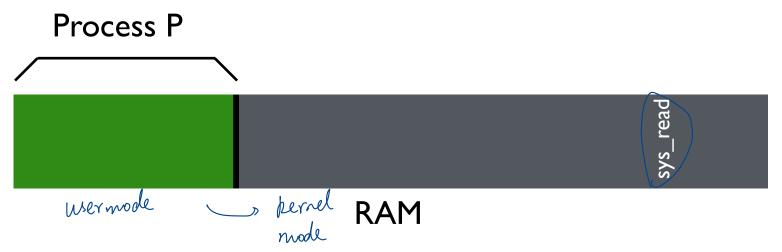
OS runs in kernel mode (not restricted)

How can process access devices?

System calls (function call implemented by OS)

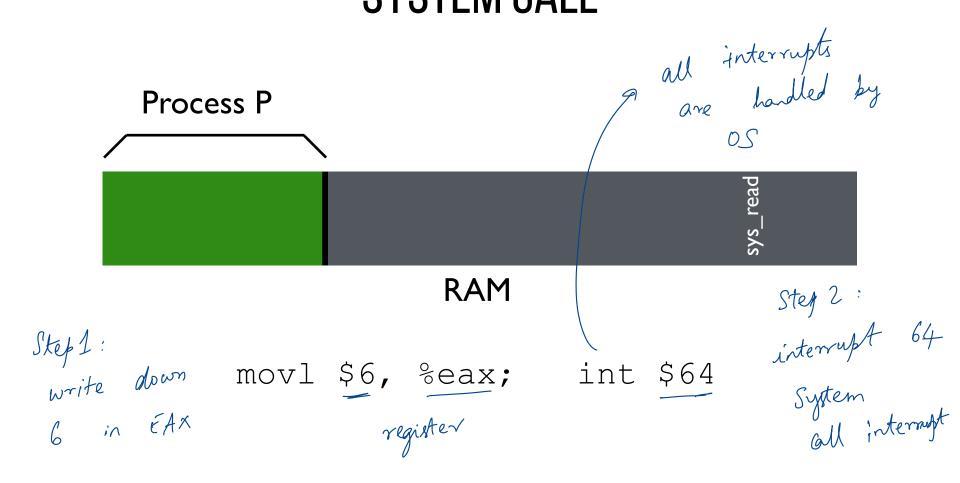
which memory
regions
which devices
etc.

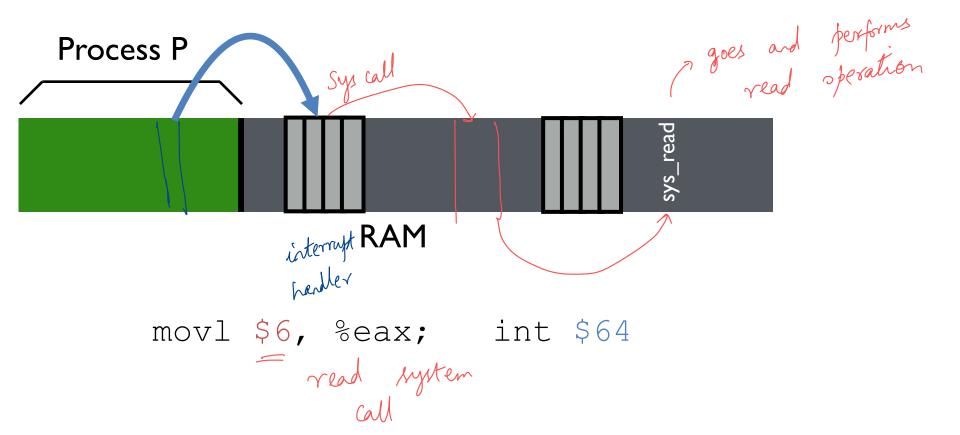


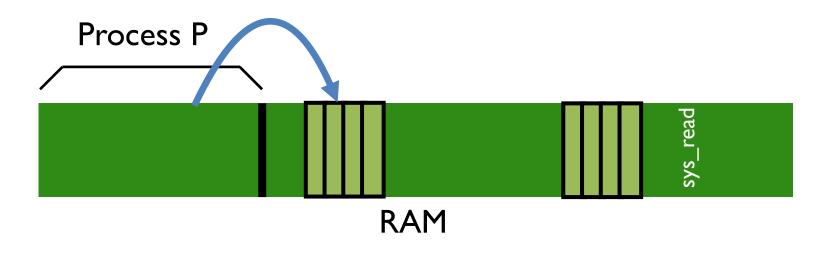


P can only see its own memory because of **user mode** (other areas, including kernel, are hidden)

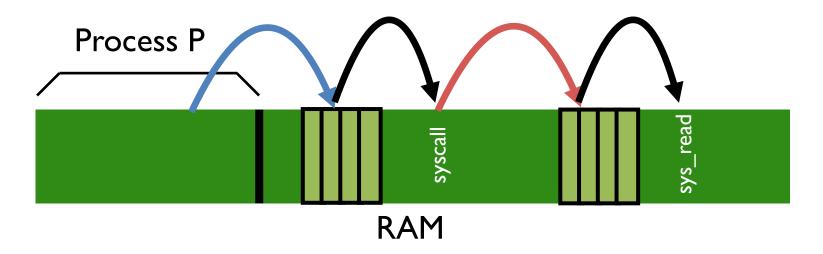
P wants to call read() but no way to call it directly





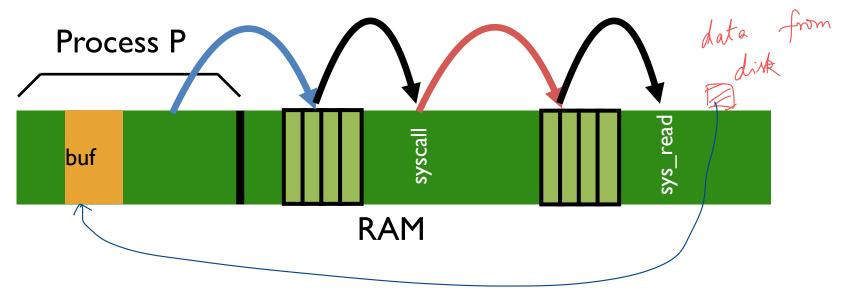






movl \$6, %eax; int \$64

Follow entries to correct system call code



Kernel can access user memory to fill in user buffer return-from-trap at end to return to Process P

### SYSCALL SUMMMARY

Separate user-mode from kernel mode for security

Syscall: call kernel mode functions

Transfer from user-mode to kernel-mode (trap)

Return from kernel-mode to user-mode (return-from-trap)

OUIZ 2



To call SYS read the instructions we used were

movl \$6, %eax int \$64

To call SYS exec what will be the instructions?

movl 
$$\frac{$9}{$64}$$
 %eax int  $\frac{$64}{$}$  — system call handler

#### https://tinyurl.com/cs537-fa24-quizlb

```
// System call numbers
#define SYS fork
#define SYS exit
#define SYS wait
                    3
#define SYS pipe
#define SYS write
                    5
#define SYS read
#define SYS close
#define SYS kill
                    8
#define SYS_exec
#define SYS open
                   10
```

## PROBLEM2: HOW TO TAKE CPU AWAY

#### **Policy**

To decide which process to schedule when

Decision-maker to optimize some workload performance metric

#### Mechanism

To switch between processes

Low-level code that implements the decision

Separation of policy and mechanism: Recurring theme in OS

### DISPATCH MECHANISM

OS runs dispatch loop

```
while (1) {
    run process A for some time-slice
    stop process A and save its context
    load context of another process B
}
```

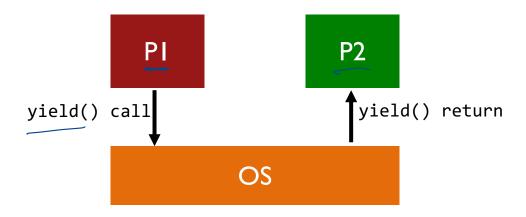
Question I: How does dispatcher gain control?

Question 2: What must be saved and restored?

# HOW DOES DISPATCHER GET CONTROL?

Option I: Cooperative Multi-tasking: Trust process to relinquish CPU through traps

- Examples: System call, page fault (access page not in main memory), or error (illegal instruction or divide by zero)
- Provide special yield() system call



### PROBLEMS WITH COOPERATIVE?

Disadvantages: Processes can misbehave

By avoiding all traps and performing no I/O, can take over entire machine Only solution: Reboot!

Not performed in modern operating systems

## TIMER-BASED INTERRUPTS

Option 2: Timer-based Multi-tasking

Guarantee OS can obtain control periodically

Enter OS by 10 ms

Enter OS by enabling periodic alarm clock

Hardware generates timer interrupt (CPU or separate chip) Example: Every 10ms User must not be able to mask timer interrupt

Program

Process A

Trap hardler

Scheduler() -> select

next rocers

Nave pernel regs (A) proc struct

restores pernel regs (B)

Switch k- stack (B)

timer interrupt
saves regs (A) to kernel
stack
jumps to interrupt handler User

Process B

Operating System

Hardware

Program
Process A

timer interrupt save regs(A) to k-stack(A) move to kernel mode jump to trap handler Operating System

Hardware

Process A

Program

timer interrupt save regs(A) to k-stack(A) move to kernel mode jump to trap handler

```
Handle the trap
Call switch() routine
save kernel regs(A) to proc-struct(A)
restore kernel regs(B) from proc-struct(B)
switch to k-stack(B)
return-from-trap (into B)
```

```
timer interrupt
                                         save regs(A) to k-stack(A)
                                         move to kernel mode
Handle the trap
                                         jump to trap handler
Call switch() routine
save kernel regs(A) to proc-struct(A)
restore kernel regs(B) from proc-struct(B)
switch to k-stack(B)
return-from-trap (into B)
                                         move to user mode
```

restore regs(B) from k-stack(B) jump to B's IP

```
Handle the trap

Call switch() routine

save kernel regs(A) to proc-struct(A)

restore kernel regs(B) from proc-struct(B)

switch to k-stack(B)

return-from-trap (into B)
```

```
timer interrupt
save regs(A) to k-stack(A)
move to kernel mode
jump to trap handler
```

restore regs(B) from k-stack(B) move to user mode jump to B's IP

## **SUMMARY**

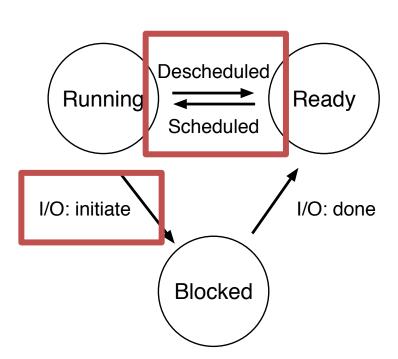
Process: Abstraction to virtualize CPU

Use time-sharing in OS to switch between processes

#### Key aspects

Use system calls to run access devices etc. from user mode

Context-switch using interrupts for multi-tasking



# POLICY? NEXT CLASS!

#### **NEXT STEPS**

Project I: Due Friday, Sept 13th

Project 2: Out Friday, Sept 13th

Waitlist? Email <a href="mailto:enrollment@cs.wisc">enrollment@cs.wisc</a> and cc me (will finalize by Thursday)