CS 744: WELD

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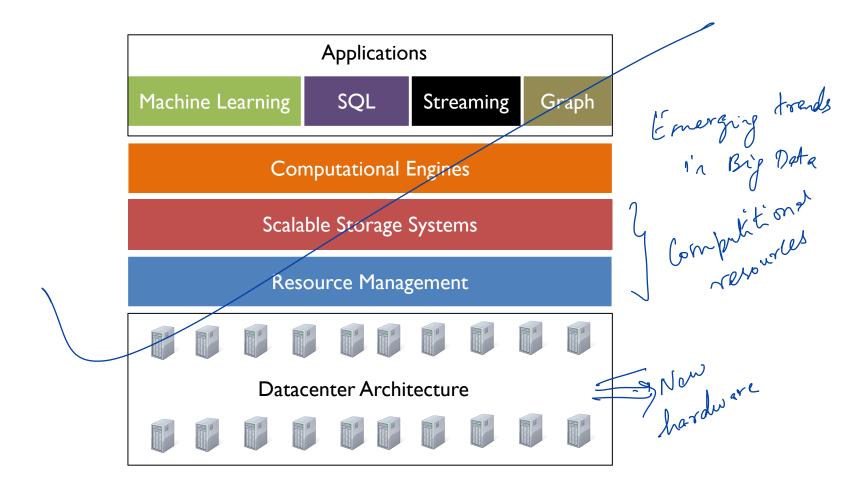
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ADMINISTRIVIA

Course Project: Check in meetings Thu, Mon

Preparation for the meeting

- what have you done so far
- a timeline for things you want to do next
- what are some specific things we can help you with



Multiple frameworks You Col · No permire - frocess From Black Scholes Multi-core machines // all inputs are vectors , $\hfill \sim$ Multiple functions and libraries ___d1 = price * strike mult d1 = np.log2(d1) + strike loz Data movement vs. compute CPU -> Men -> CpU 256GB nemore Alternate approaches? CPU CPU CPV CQU prei CPY Cache pricex a1 ····· log nd. log (di)

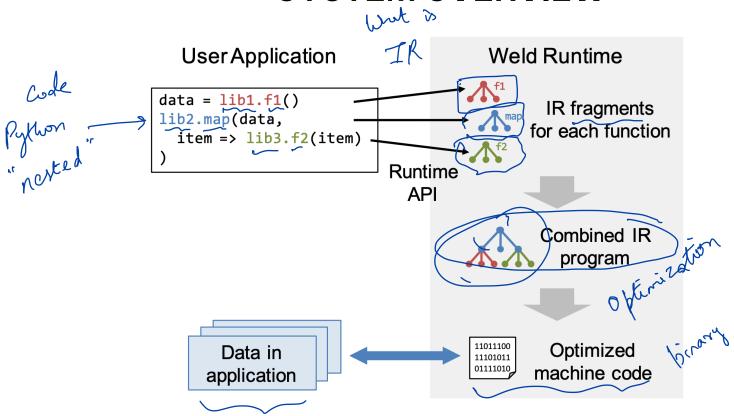
GOALS

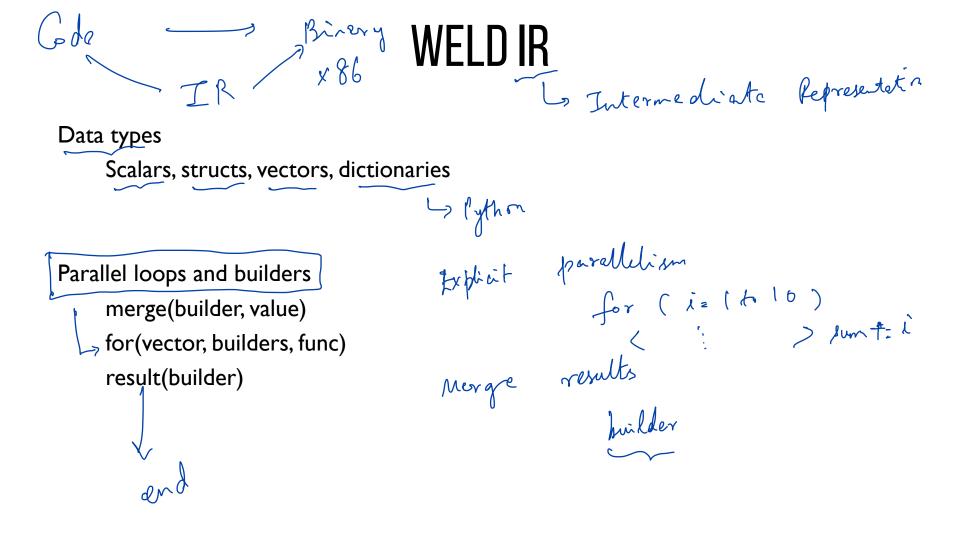
Work with independently written libraries

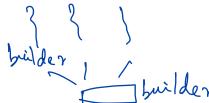
Enable the most impactful cross-library optimizations

Integrate incrementally into existing systems

SYSTEM OVERVIEW





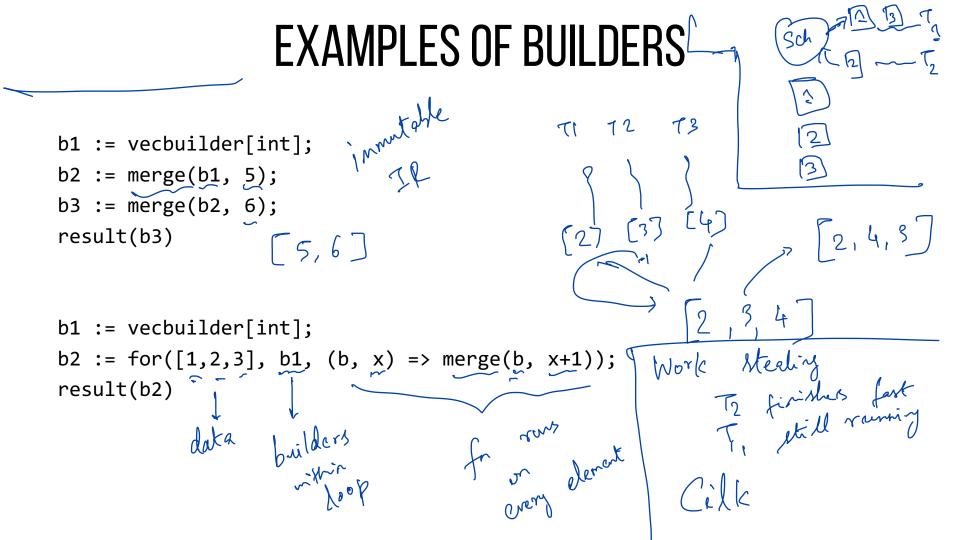


BUILDER TYPES

Builder Types	
<pre>vecbuilder[T]</pre>	Builds a vec [T] by appending merged values of type T
<pre>merger[T,func,id]</pre>	Builds a value of type T by merging val- ues using a commutative function func and an identity value id
<pre>dictmerger[K,V,func]</pre>	Builds a dict [K,V] by merging {K,V} pairs using a commutative function
<pre>vecmerger[T,func]</pre>	Builds a vec [T] by merging {index, T } elements into specific cells in the vector using a commutative function
<pre>groupbuilder[K,V]</pre>	Builds a dict [K, vec [V]] from values of type {K, V} by grouping them by key

Accumulators Ls Spark Ls Power Greph

vector 100 0 merge (vec, o) marge (vec, 100) ver merger (+) 15 merge (vec, 0, 5) merge (vec, 0, 10)



MULTIPLE BUILDER

6 de data := [1,2,3]; F2,3,43 r1 := map(data, x =>(x+1); r2 := reduce(data, 0, (x, y) => x+y); Sear on ce to o Sear on ce to o produce one produces reduce Anak data := [1,2,3]; result(for(data, {vecbuilder[int], merger(+)}, (bs, x) = >{merge(bs.0, x+1), merge(bs.1, x)}))

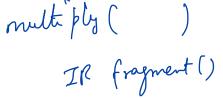
RIINTIME API

numpy.log() IR foragmet()

API to express IR fragments in libraries

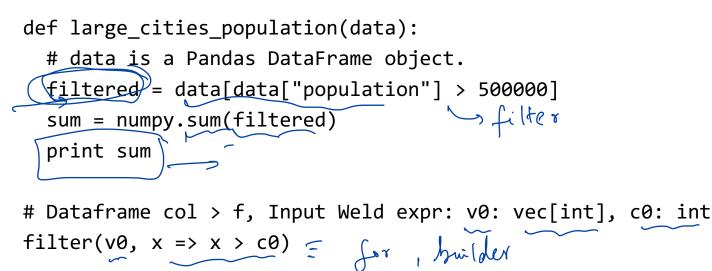
Capture dependencies across functions/libraries.

def square(self, arg): # Programatics? # Programatically construct an IR expression. To deb

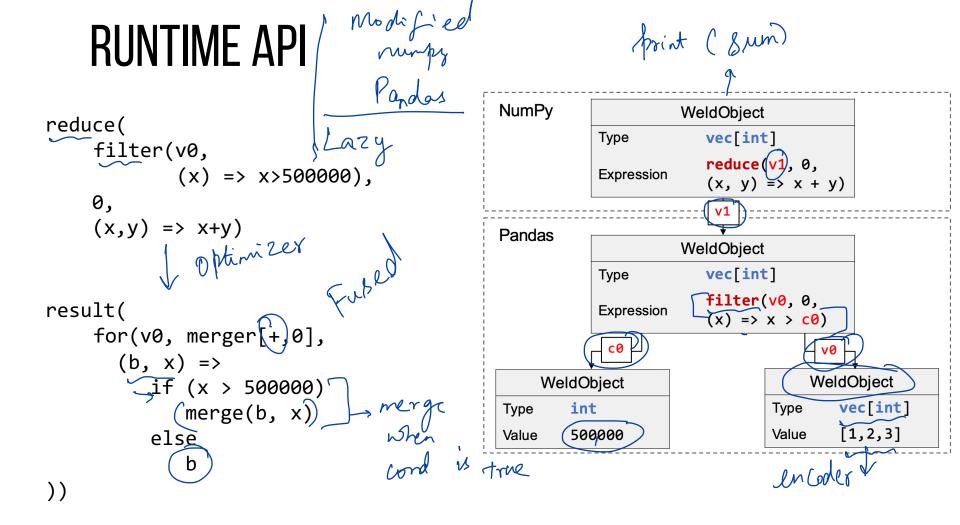


print num print frum eval (TR)

RUNTIME API



Numpy.sum Input Weld expr: v0: vec[int] reduce(v0, 0, (x, y) => x+y) $\subseteq \int_{0}^{0} r$, builder



OPTIMIZATIONS -> Extensively studied



for () for () (for () for () ([Carte] [(for () (for ()

fer

for () t -> for ()

Loop Fusion

Fuse adjacent loops when output of one loop is input of other

Fuse multiple passes over the same vector

Loop Tiling Break nested loops into blocks E Linear Algebra

OPTIMIZATIONS

Vectorization

Transform loops to use vector instructions

Common subexpression elimination

Transforms to not run the same computation multiple times

 $t_{mp} = b \neq c$ $a = t_{mp} \neq g$ $e = t_{mp} \neq f$

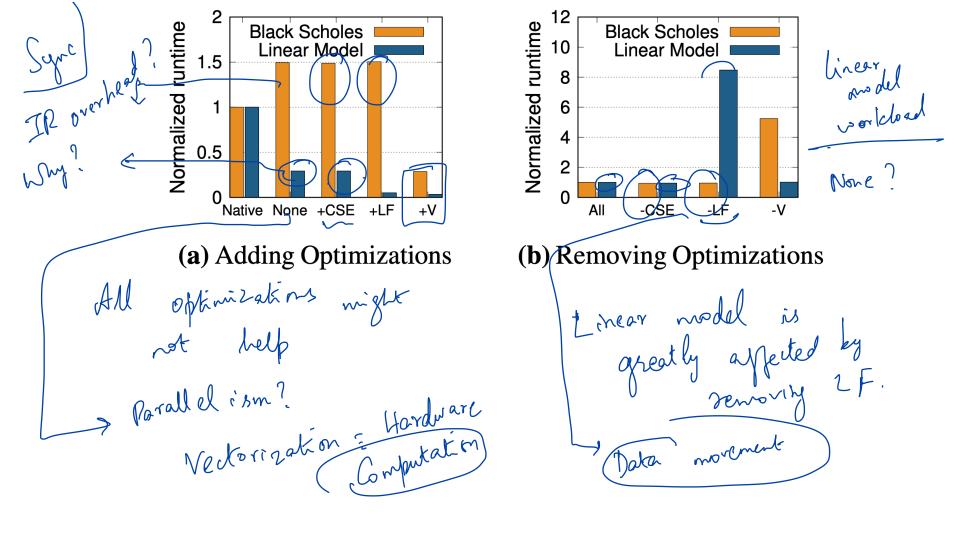
for (i. 1 to 10, it=1) Inst

for (i=1to 10,1=4) Avx Instr Z # 7

> Muttiple passes

DISCUSSION

https://forms.gle/DxHfcmuS2juK1tuE7



What are some possible limitations of Weld as described in the paper?

to more libraries! Ly Scale to get perf win Backend to 0 La Full deterministic La Asyne SGD (Data men doesn't fit is 1) Fit is 1) enord. - Vebug -> Fault dolerance } Restricted

What does the Weld paper tell us about the using scale-up vs. scale-out?

NEXT STEPS

Next class: PyWren

Project check-in meetings