CS/ECE 252: INTRODUCTION TO COMPUTER ENGINEERING UNIVERSITY OF WISCONSIN—MADISON

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Midterm Examination 1

In Class (50 minutes)

Friday, February 15, 2013

Weight: 17.5%

NO: BOOK(S), NOTE(S), OR CALCULATORS OF ANY SORT.

The exam has 108 pages. **Circle your final answers**. Plan your time carefully since some problems are longer than others. You **must turn in the pages 1-8**.

LAST NAME:	
FIDCT NIANAE.	
FIRST NAME:	
ID#	

Problem	Maximum Points	Points Earned
1	9	
2	4	
3	3	
4	2	
5	3	
6	2	
7	1	
8	2	
9	4	
Total	30	

Problem 1 (9 Points)

For the following problems, circle the best answer. Choose only one answer per question.

- i There are **n** bits to represent all the students in a class, and the students are numbered 0-67. What is the value of **n**?
 - a 5
 - b 4
 - c 6
 - d 7
- ii Which of the following two's complement binary numbers is a multiple of 2 (even).
 - a 1010101
 - b 1101011
 - c 0111010
 - d 1110111
- iii Which of the following are properties of an algorithm?
 - a Each step has to be precisely stated.
 - b The program implementing it has to use a minimum amount of memory.
 - c The procedure should terminate.
 - d a and c.
 - e b and c.
- iv Which of the following have **two** representations for 0?
 - a Two's complement
 - b Signed magnitude
 - c All of the above
 - d None of the above
- v Two computers A and B are identical except that A has a multiply instruction and B does not. Both have add instructions. Assume the multiply and add instructions take the same amount of time to operate on two numbers. Which of the following is true about computers A and B?
 - a B can compute all the same problems as A, in the same amount of time.
 - b B can compute all the same problems as A, in the same amount of time, given enough memory.
 - c B can compute all the same problems as A, but might take longer.
 - d A can compute more types of problems than B.

vi	Which	of the following is the fixed point binary representation of $4\frac{3}{8}$?
	a	0100.011
	b	010.011
	c	0110.101
	d	0100.101
vii	What	is the largest positive value that can be represented by an 9-bit two's complement
	intege	r?
	a	512
	b	511
	c	255
	d	256
viii	What	is the most negative value that can be represented by a 4-bit two's complement
	intege	r?
	a	-7
	b	-8
	c	-16
	d	-15
ix	When	using two's complement notation, which of the following bit patterns represent the
	negati	on of 01011010?
	a	10100101

b 10010110c 10010100d 10100110

	a	Convert the ASCII string "Q@t5" to its hexadecimal representation. Onl characters within the quotation marks and assume that the string is null te See attached ASCII to hexadecimal table.	-
		0x51 40 74 35 00	
	b	Convert the following binary code into an ASCII string. 0101 1001 0011 0000 0110 1100 0010 1010 0000 0000	(2 Points)
		Y01*	
Pr	Co	em 3 onsider the 8-bit binary pattern 1001 0011. What decimal (base 10) value dowe interpret it as:	(3 Points) pes it represent
	a	An unsigned integer?	(1 Point)
		147	
	b	A signed magnitude integer?	(1 Point)
		-19	
	c	A two's complement integer?	(1 Point)
		-109	

(4 Points)

Problem 2

Problem 4 (2 Points)

Perform binary arithmetic for the following pairs of 2's complement numbers and specify whether or not there is an overflow. Illustrate your answer by converting the operands and result to decimal. Assume the result has the same number of bits as the operands.

```
a 110 -2
+ 110 -2
-----
100 -4 No overflow
```

Problem 5 (3 Points)

Suppose we have two operands X and Y, where X = 0x34 and Y = 0xAF.

a Convert X and Y into **binary**.

(1 Point)

X=0011 0100 Y=1010 1111

b What is the result of **X AND Y**? Give your answer in **hexadecimal.** (1 Point)

 $0010\ 0100 = 0x24$

c What is the result of **X OR Y**? Give your answer in **hexadecimal.** (1 Point)

 $1011\ 1111 = 0xBF$

Problem 6 (2 Points)

Show the result of performing the following bitwise logic operations:

a (NOT(0011)) AND (0110 OR 1011)

1100

b NOT(01001 OR (01100 AND 10100))

10010

Problem 7 (1 Point)

What is the difference between an instruction set architecture (ISA) and a microarchitecture? Give your answer in **one** sentence.

A microarchitecture is the particular way an ISA is implemented on a processor.

Problem 8 (2 Points)

Suppose we have two 4-bit 2's complement integers 0101 and 1010. Express their sum as an 8-bit 2's complement number.

1111 1111

Problem 9 (4 Points)

Below is a single-precision floating point representation of some value. Note that the exponent bits have been left out.

Recall that the bits for the IEEE floating point number are as follows:

Sign (1 bit) Exponent (8 bits) Fraction (23 bits)

where
$$N = (-1)^{sign} * 2^{exponent-127} * 1$$
. fraction

a Find the smallest value for the exponent field such that the represented number is greater than the decimal value **4.0**. Give your answer in **binary**. (3 Points)

$$(-1)^0 * 2^{\text{exponent}-127} * 1.110001_2 > 4$$

 $\frac{113}{64} > 2^2 * 2^{127-\text{exponent}}$
exponent $> 129 - \log(113/64)$
min val or exponent $= 129 = > 1000\ 0001$

b What is the decimal value of the number represented in part a? (1 Point)

$$(-1)^0 * 2^2 * 1.110001_2 = 111.0001_2 = 7\frac{1}{16}$$