Venkatesh Srinivasan

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Research Interests

Program synthesis, binary analysis and rewriting, static program analysis, dynamic program analysis

Education

University of Wisconsin–Madison, Madison, WI, USA
Ph.D. in Computer Sciences *March 2017*

- Advisor: Prof. Thomas Reps
- Thesis topic: Synthesis of Machine Code: Algorithms and Applications

M.S. in Computer Sciences *May 2012*

College of Engineering, Guindy (CEG), Anna University–Chennai, Chennai, India
B.E. in Computer Science and Engineering *May 2010*

Conference Publications

· Venkatesh Srinivasan, Ara Vartanian, and Thomas Reps. Model-Assisted Machine-Code Synthesis. *Submitted for conference publication, April 2017*

· Venkatesh Srinivasan and Thomas Reps. An Improved Algorithm for Slicing Machine Code. *Object-Oriented Programming, Systems, Languages and Applications (OOPSLA), 2016* (artifact evaluated)

· Venkatesh Srinivasan, Tushar Sharma, and Thomas Reps. Speeding Up Machine-Code Synthesis. *Object-Oriented Programming, Systems, Languages and Applications (OOPSLA), 2016*

· Venkatesh Srinivasan and Thomas Reps. Partial Evaluation of Machine Code. *Object-Oriented Programming, Systems, Languages and Applications (OOPSLA), 2015*

· Venkatesh Srinivasan and Thomas Reps. Synthesis of Machine Code from Semantics. *Programming Language Design and Implementation (PLDI), 2015*

· Venkatesh Srinivasan and Thomas Reps. Recovery of Class Hierarchies and Composition Relationships from Machine Code. *Compiler Construction (CC), 2014*

Work Experience

University of Wisconsin–Madison January 2012 - March 2017
Research Assistant: Advised by Prof. Thomas Reps *Madison, WI*

Riverbed Technology June 2011 - August 2011
Cloud-Storage Engineering Intern *Sunnyvale, CA*

Microsoft India Development Center (MSIDC) May 2009 - July 2009
Program-Manager Intern *Hyderabad, India*

Teaching Experience

University of Wisconsin–Madison

· Guest Lecturer for "Introduction to Compilers and Programming Languages" (CS 536)
Senior-undergraduate-level course; Instructor: Prof. Thomas Reps *Spring 2017*

· Guest Lecturer for "Construction of Compilers" (CS 701)
Graduate-level course; Instructor: Prof. Thomas Reps *Fall 2015, Fall 2016*

· Teaching Assistant for "Introduction to Compilers and Programming Languages" (CS 536)
Senior-undergraduate-level course; Instructor: Prof. Susan Horwitz *Spring 2011*

· Teaching Assistant for "Introduction to Operating Systems" (CS 537)
Senior-undergraduate-level course; Instructors: Prof. Shan Lu, Prof. Mike Swift *Fall 2010, Fall 2011*

Honors and Awards

· Alumni Scholarship for incoming graduate students, Computer Sciences, University of Wisconsin–Madison

· Second-rank holder in the Department of Computer Science and Engineering, CEG, Anna University–Chennai

Service

· External Reviewer: Conf. on Tools and Algorithms for the Construction and Analysis of Systems (TACAS), 2016

Technical Skills

Computer Languages	C++, C, Python, Scheme, OCaml, IA-32
Tools	CodeSurfer, Pin, LLVM