********** SPICE DECK **********

*** Generated from xnof201.mag by magicsspicegenerator'
*** technology: scmos
*** timestamp: 598548837
*** version: dlm V2.0
*** Capacitance Threshold = 5000000 units
*** Resistance Scale = 1000 units

M12x37 vdd A1 23 vdd PMOS W=29.0U L=2.0U
M20x37 23 B1 O vdd PMOS W=29.0U L=2.0U
M28x37 O 23 120 vdd PMOS W=26.0U L=2.0U
M32x37 120 24 vdd vdd PMOS W=26.0U L=2.0U
M40x37 vdd B1 24 vdd PMOS W=26.0U L=2.0U
M12x1 gnd A1 23 gnd NMOS W=23.0U L=2.0U
M20x1 23 24 O gnd NMOS W=23.0U L=2.0U
M28x1 O 23 24 gnd NMOS W=23.0U L=2.0U
M36x1 24 B1 gnd gnd NMOS W=23.0U L=2.0U

2-micron delay information.

Path	Timing	Nominal Case		Worst Case	
		No Load	1pF	No Load	1pF
A1,B1---O	Pd 0--1	2.01	4.13	4.42	9.09
	Pd 1--0	1.75	3.45	3.85	7.59
	Tr 0--1	3.47	8.15	7.63	17.93
	Tf 1--0	1.60	4.17	3.52	9.17

3-micron delay information.

Path	Timing	Nominal Case		Worst Case	
		No Load	1pF	No Load	1pF
A1,B1---O	Pd 0--1	4.86	8.43	9.45	14.20
	Pd 1--0	5.03	7.08	9.06	12.70
	Tr 0--1	8.22	14.90	13.97	23.80
	Tf 1--0	3.78	6.92	6.34	10.91

Cell Device Sizes

Transistor	Width (λ)	Length (λ)
50	23	2
52	23	2
53	23	2
51	23	2
20	29	2
22	29	2
23	26	2
24	26	2
21	26	2

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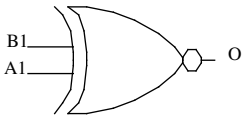
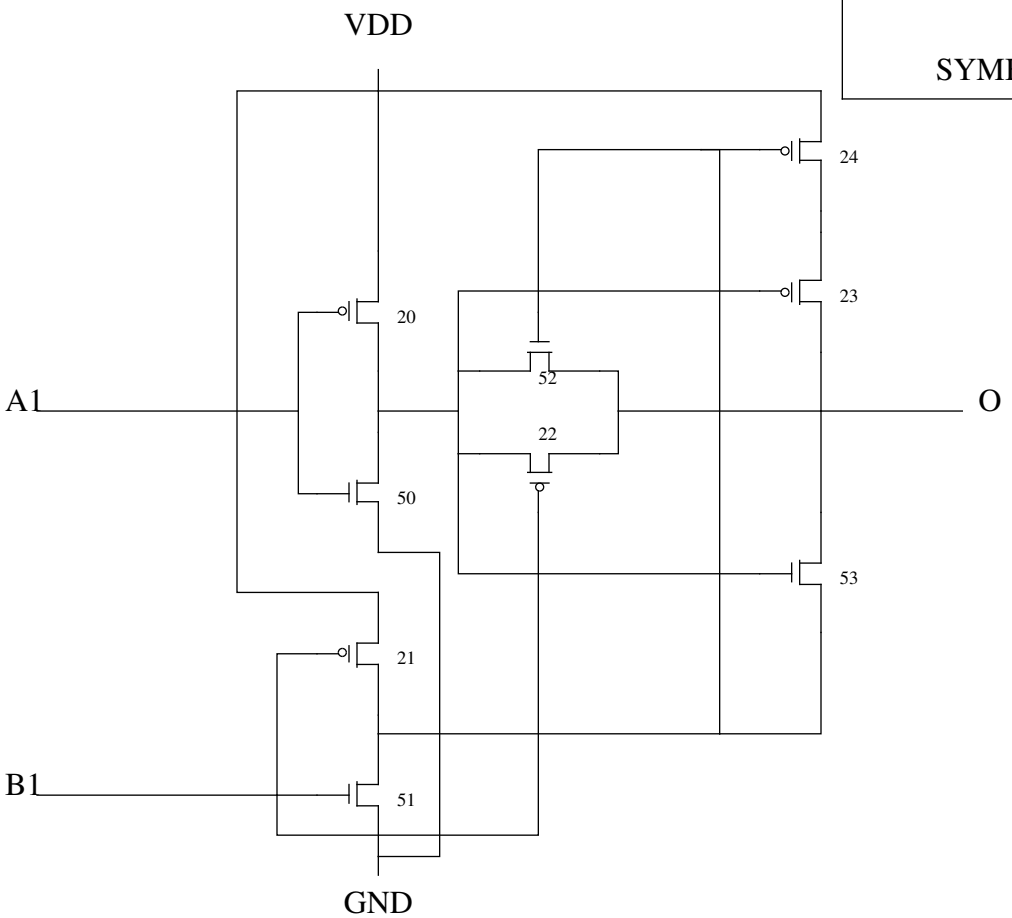
SCALABLE CMOS (SCMOS) DOUBLE METAL I / C STANDARD CELL LIBRARY	**xnof201** 2_INPUT_EX-NOR	DATE: Dec 21,1988 VERSION: dlmV2.0



SYMBOL



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TERMINAL LOCATION (In Lambda)

GRID	PIN_NAME	Y_LOC	CLASS
1	A1	29	1
2	FeedThru	----	----
3	O	0	0
4	FeedThru	----	----
5	FeedThru	----	----
6	B1	31	1

LOGIC EQUATIONS: O = A xnor B

Truth Table

A	B	O
0	0	1
1	1	1