********** SPICE DECK CONTINUED **********

C9 56 gnd 146.17fF
C10 120 gnd 146.14fF
C11 121 gnd 93.42fF
C13 150 gnd 13.55fF
C14 O gnd 93.92fF
C15 151 gnd 13.55fF
C16 152 gnd 13.55fF

********** SPICE DECK **********

*** Generated from aof3201.mag by magic's spice generator
*** technology: scmos
*** extraction style: lambda=1.0
*** units per lambda: dlm V2.20
*** timestamp: 628905226
*** version: dlm V2.2
*** Capacitance Threshold = 5000 units
*** Resistance Scale = 1000 units

M8x43 120 50 56 vdd PMOS W=27.0U L=2.0U
M16x43 56 B1 120 vdd PMOS W=27.0U L=2.0U
M24x43 120 C2 121 vdd PMOS W=27.0U L=2.0U
M32x43 121 D2 120 vdd PMOS W=27.0U L=2.0U
M52x46 121 E3 vdd vdd PMOS W=24.0U L=2.0U
M60x46 121 F3 vdd vdd PMOS W=24.0U L=2.0U
M8x4 56 50 150 gnd NMOS W=24.0U L=2.0U
M12x4 150 B1 gnd gnd NMOS W=24.0U L=2.0U
M72x39 O 56 vdd vdd PMOS W=31.0U L=2.0U
M32x4 151 C2 gnd gnd NMOS W=24.0U L=2.0U
M36x4 151 D2 56 gnd NMOS W=24.0U L=2.0U
M44x4 56 E3 152 gnd NMOS W=24.0U L=2.0U
M48x4 152 F3 gnd gnd NMOS W=24.0U L=2.0U
M72x4 O 56 gnd gnd NMOS W=22.0U L=2.0U
C1 120 vdd 5.26fF
C2 121 vdd 5.31fF
C3 50 gnd 6.90fF
C4 B1 gnd 7.76fF
C5 C2 gnd 7.85fF
C6 D2 gnd 8.10fF
C7 E3 gnd 8.25fF
C8 F3 gnd 9.30fF

2-micron delay information.

Path	Timing	Nominal Case		Worst Case	
		No Load	1pF	No Load	1pF
A1,B1---O	Pd 0--1	1.29	2.79	3.53	6.39
	Pd 1--0	2.67	3.71	6.73	8.74
	Tr 0--1	1.28	5.01	2.82	8.46
	Tf 1--0	1.46	3.53	2.40	5.92
C2,D2---O	Pd 0--1	1.70	3.04	5.05	7.83
	Pd 1--0	2.73	3.71	6.73	8.90
	Tr 0--1	1.22	5.23	3.01	8.52
	Tf 1--0	2.56	4.10	2.82	6.30
E3,F3---O	Pd 0--1	2.03	3.28	5.58	8.50
	Pd 1--0	2.85	3.94	6.73	9.15
	Tr 0--1	1.42	5.39	3.06	8.73
	Tf 1--0	2.58	4.27	2.94	6.52

Cell Device Sizes

Transistor	Width (λ)	Length (λ)
52	24	2
53	24	2
54	24	2
56	22	2
55	24	2
50	24	2
51	24	2
20	27	2
21	27	2
22	27	2
23	27	2
26	31	2
25	24	2
24	24	2

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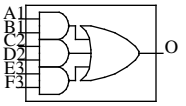
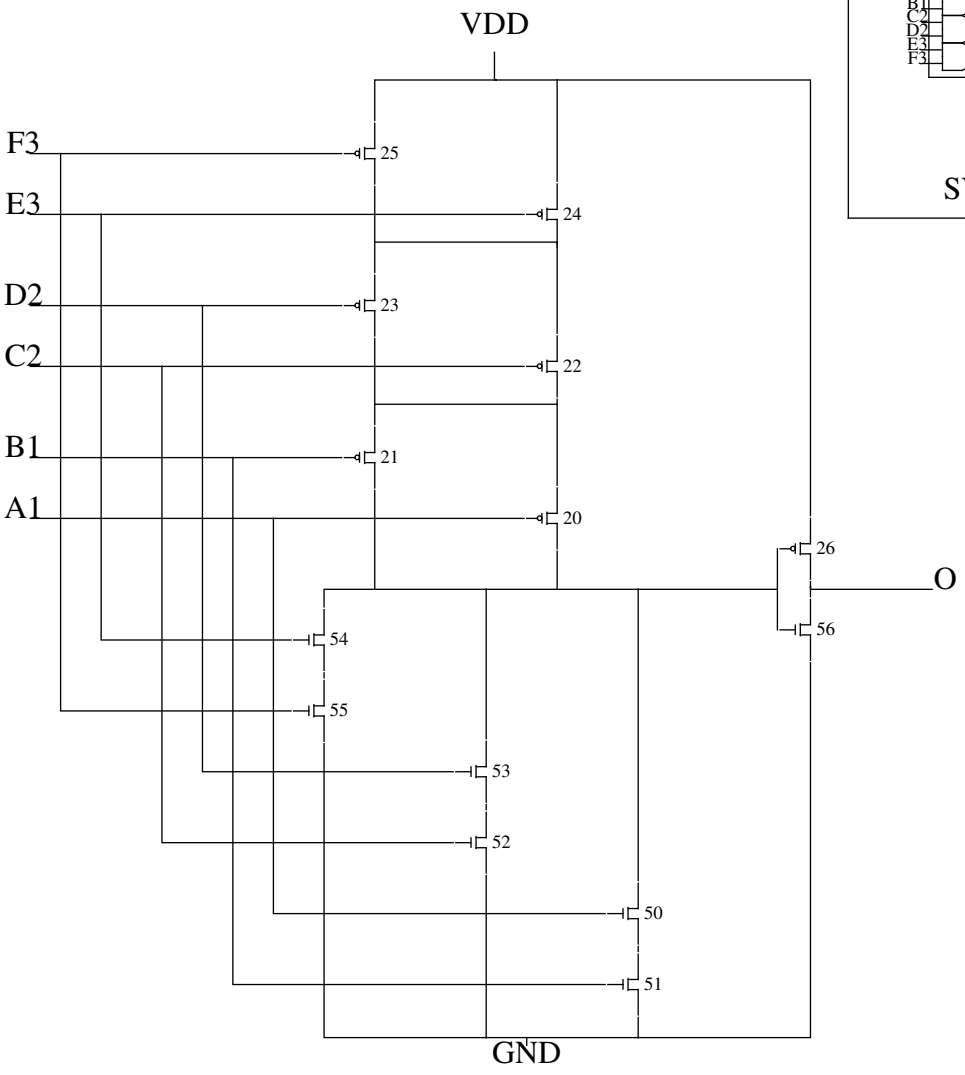
ADVANCED MICROELECTRONICS DIVISION

SCALABLE CMOS (SCMOS) DOUBLE METAL I / C STANDARD CELL LIBRARY	**aof3201** 3/2_AND/OR	DATE: Mar 2,1990 VERSION: dlmV2.2



SYMBOL



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TERMINAL LOCATION (In Lambda)

GRID	PIN_NAME	Y_LOC	CLASS
1	A1	36	1
2	FeedThru	----	----
3	B1	39	1
4	C2	38	2
5	D2	38	2
6	FeedThru	----	----
7	E3	37	3
8	F3	32	3
9	FeedThru	----	----
10	O	35	0

LOGIC EQUATIONS: O = AB + CD + EF

Truth Table

AB	CD	EF	O
-	-	1	1
-	1	-	1
1	-	-	1