********** SPICE DECK **********

*** Generated from buff100.mag by magic's spice generator
*** technology: scmos
*** extraction style: lambda=1.0
*** units per lambda: dlm V2.20
*** timestamp: 628905226
*** version: dlm V2.2
*** Capacitance Threshold = 5000 units
*** Resistance Scale = 1000 units

M8x43 O 52 vdd vdd PMOS W=18.0U L=2.0U
M21x44 120 gnd vdd vdd PMOS W=18.0U L=2.0U
M27x48 120 50 52 vdd PMOS W=14.0U L=7.0U
M8x11 O 52 gnd gnd NMOS W=14.0U L=2.0U
M24x11 150 vdd gnd gnd NMOS W=8.0U L=2.0U
M28x11 150 50 52 gnd NMOS W=8.0U L=6.0U
C2 52 gnd 63.80fF
C3 O gnd 62.35fF
C4 50 gnd 12.88fF
C5 120 gnd 23.18fF
C6 150 gnd 5.85fF

2-micron delay information.

Path	Timing	Nominal Case		Worst Case	
		No Load	1pF	No Load	1pF
A1---O	Pd 0--1	2.73	5.24	6.81	12.10
	Pd 1--0	3.20	5.12	8.63	12.61
	Tr 0--1	1.47	6.90	3.08	13.19
	Tf 1--0	2.17	5.18	3.69	8.78

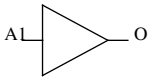
Cell Device Sizes

Transistor	Width (λ)	Length (λ)
52	14	2
50	8	6
20	14	7
22	18	2

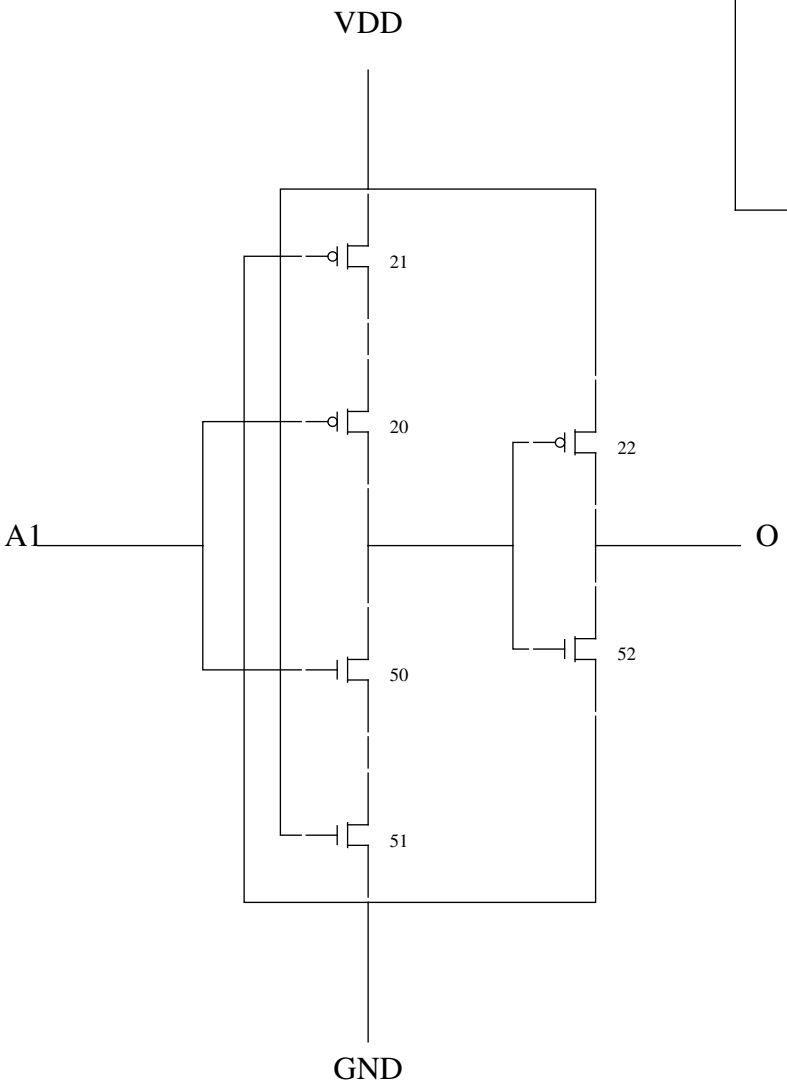
Institute for Technology Development

ADVANCED MICROELECTRONICS DIVISION

SCALABLE CMOS (SCMOS) DOUBLE METAL I / C STANDARD CELL LIBRARY	**buff100** HI_Z_BUFFER	DATE: Mar 2,1990 VERSION: dlmV2.2



SYMBOL



TERMINAL LOCATION (In Lambda)

GRID	PIN_NAME	Y_LOC	CLASS
1	O	39	0
2	FeedThru	----	----
3	FeedThru	----	----
4	A1	42	1
5	FeedThru	----	----

LOGIC EQUATIONS: O = A

Truth Table

A	O
1	1