********** SPICE DECK **********

*** Generated from buff101.mag by magic's spice generator
*** technology: scmos
*** extraction style: lambda=1.0
*** units per lambda: dlm V2.20
*** timestamp: 628905226
*** version: dlm V2.2
*** Capacitance Threshold = 5000 units
*** Resistance Scale = 1000 units

M8x39 51 A1 vdd vdd PMOS W=11.0U L=2.0U
M16x39 O 51 vdd vdd PMOS W=31.0U L=2.0U
M8x18 51 A1 gnd gnd NMOS W=5.0U L=2.0U
M16x8 O 51 gnd gnd NMOS W=16.0U L=2.0U
C1 51 gnd 49.33fF
C3 A1 gnd 9.91fF
C4 O gnd 86.44fF

2-micron delay information.

Path	Timing	Nominal Case		Worst Case	
		No Load	1pF	No Load	1pF
A1---O	Pd 0--1	1.32	2.47	3.51	6.54
	Pd 1--0	1.29	2.34	4.02	6.24
	Tr 0--1	1.05	4.81	2.70	7.64
	Tf 1--0	1.77	3.84	2.54	6.60

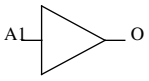
Cell Device Sizes

Transistor	Width (λ)	Length (λ)
51	16	2
21	31	2

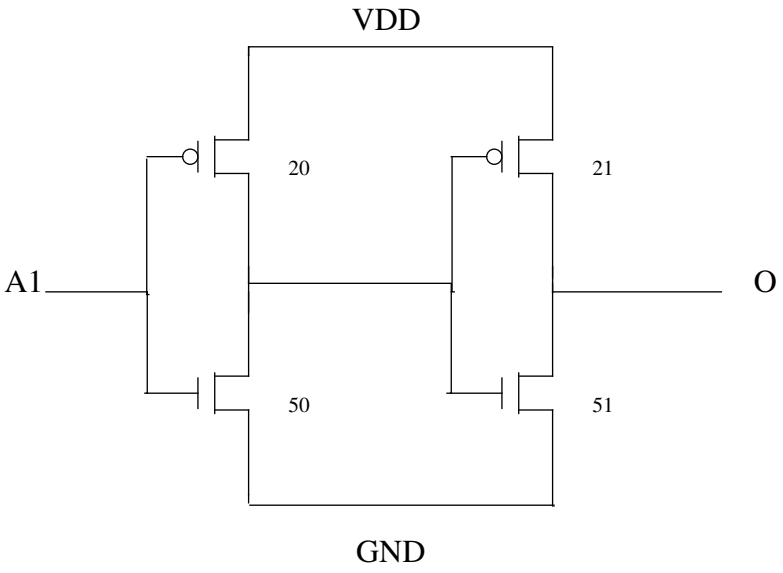
Institute for Technology Development

ADVANCED MICROELECTRONICS DIVISION

SCALABLE CMOS (SCMOS) DOUBLE METAL I / C STANDARD CELL LIBRARY	**buff101** NON_INVERTING_BUFFER	DATE: Mar 2,1990 VERSION: dlmV2.2



SYMBOL



Institute for Technology Development

ADVANCED MICROELECTRONICS DIVISION

SCALABLE CMOS (SCMOS) DOUBLE METAL I / C STANDARD CELL LIBRARY	**buff101** NON_INVERTING_BUFFER	DATE: Mar 2,1990 VERSION: dlmV2.2

TERMINAL LOCATION (In Lambda)

GRID	PIN_NAME	Y_LOC	CLASS
1	A1	56	1
2	FeedThru	----	----
3	O	26	0

LOGIC EQUATIONS: O = A

Truth Table

A	O
1	1