********** SPICE DECK **********

*** Generated from delf011.mag by magic's spice generator
*** technology: scmos
*** extraction style: lambda=1.0
*** units per lambda: dlm V2.20
*** timestamp: 628905226
*** version: dlm V2.2
*** Capacitance Threshold = 5000 units
*** Resistance Scale = 1000 units

M21x50 120 51 vdd vdd PMOS W=10.0U L=5.0U
M30x50 120 gnd 23 vdd PMOS W=20.0U L=2.0U
M6x44 51 50 vdd vdd PMOS W=11.0U L=5.0U
M45x44 54 23 vdd vdd PMOS W=7.0U L=5.0U
M60x47 O 54 vdd vdd PMOS W=23.0U L=2.0U
M9x19 51 50 gnd gnd NMOS W=4.0U L=5.0U
M38x23 54 23 gnd gnd NMOS W=4.0U L=8.0U
M18x12 150 51 gnd gnd NMOS W=6.0U L=8.0U
M28x12 150 vdd 23 gnd NMOS W=6.0U L=2.0U
M52x15 O 54 gnd gnd NMOS W=12.0U L=2.0U
C1 54 gnd 41.07fF
C3 51 gnd 53.77fF
C4 120 gnd 26.14fF
C5 23 gnd 63.93fF
C6 50 gnd 10.91fF
C7 O gnd 66.08fF

2-micron delay information.

Path	Timing	Nominal Case		Worst Case	
		No Load	1pF	No Load	1pF
A1---O	Pd 0--1	9.63	12.22	21.37	26.11
	Pd 1--0	9.12	10.97	20.96	24.92
	Tr 0--1	1.62	5.66	3.56	11.61
	Tf 1--0	2.10	5.36	3.25	9.07

Cell Device Sizes

Transistor	Width (λ)	Length (λ)
50	4	5
51	6	8
54	12	2
53	4	8
52	6	2
20	11	5
21	10	5
22	20	2
24	23	2
23	7	5

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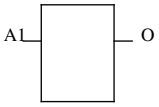
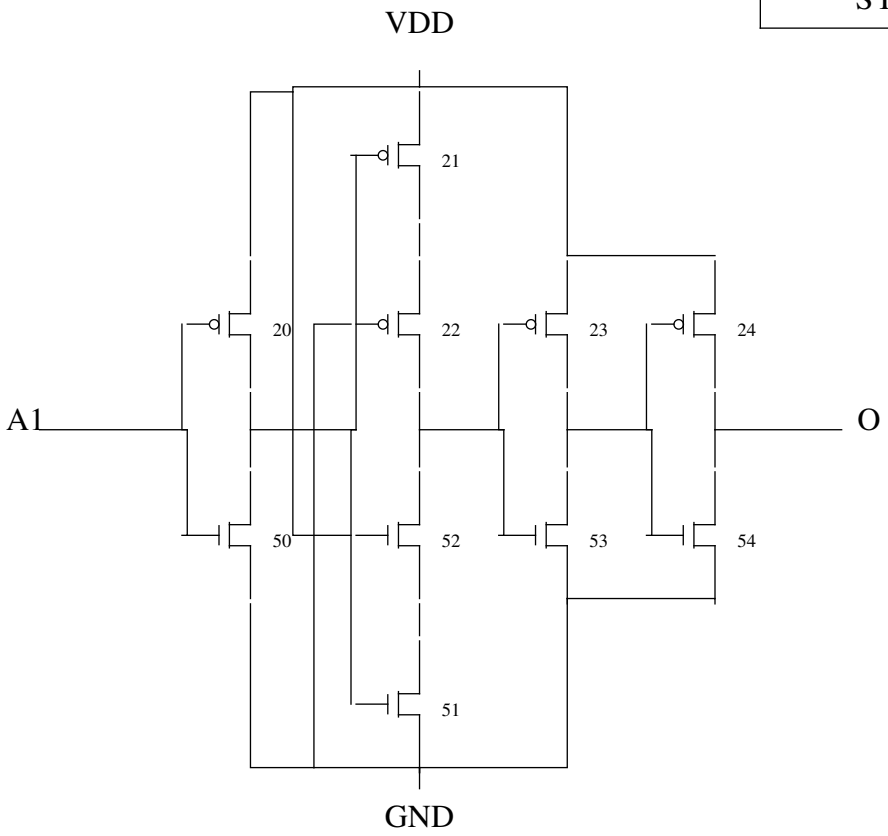
SCALABLE CMOS (SCMOS) DOUBLE METAL I / C STANDARD CELL LIBRARY	**delf011** DELAY_CELL	DATE: Mar 2,1990 VERSION: dlmV2.2



SYMBOL



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TERMINAL LOCATION (In Lambda)

GRID	PIN_NAME	Y_LOC	CLASS
1	A1	28	1
2	FeedThru	----	----
3	FeedThru	----	----
4	FeedThru	----	----
5	FeedThru	----	----
6	FeedThru	----	----
7	FeedThru	----	----
8	O	30	0
9	FeedThru	----	----

LOGIC EQUATIONS: O = A

Truth Table

A	O
1	1