********** SPICE DECK **********

*** Generated from lrbf202.mag by magic's spice generator
*** technology: scmos
*** extraction style: lambda=1.0
*** units per lambda: dlm V2.20
*** timestamp: 636933167
*** version: dlm V2.2
*** Capacitance Threshold = 5000 units
*** Resistance Scale = 1000 units

M8x39 52 21 vdd vdd PMOS W=23.0U L=2.0U
M16x39 one 21 vdd vdd PMOS W=23.0U L=2.0U
M24x39 one 21 vdd vdd PMOS W=23.0U L=2.0U
M32x39 one 21 vdd vdd PMOS W=23.0U L=2.0U
M40x39 one 21 vdd vdd PMOS W=23.0U L=2.0U
M8x12 21 52 gnd gnd NMOS W=11.0U L=2.0U
M16x12 21 21 gnd gnd NMOS W=11.0U L=2.0U
M32x12 zero 52 gnd gnd NMOS W=16.0U L=2.0U
M40x12 zero 52 gnd gnd NMOS W=16.0U L=2.0U
C2 21 gnd 72.27fF
C3 52 gnd 60.28fF
C4 one gnd 85.37fF
C5 zero gnd 28.84fF

Cell Device Sizes

Transistor	Width (λ)	Length (λ)
50	11	2
51	11	2
52	32	2
20	23	2
21	92	2

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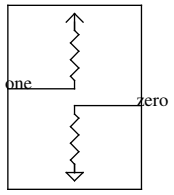
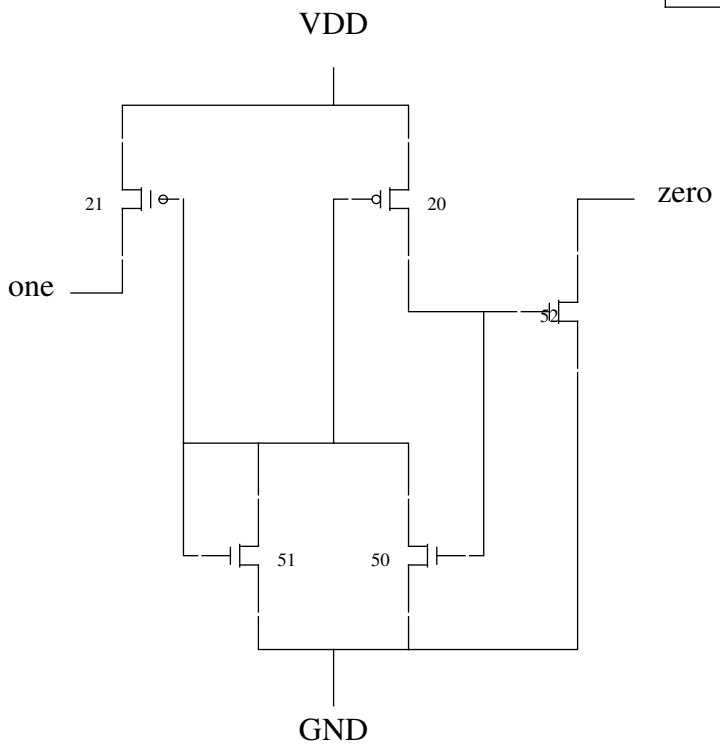
SCALABLE CMOS (SCMOS) DOUBLE METAL I / C STANDARD CELL LIBRARY	**lrbf202** LOGIC_REFERENCE_CELL	DATE: Mar 9,1990 VERSION: dlmV2.2



SYMBOL



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SCALABLE CMOS (SCMOS) DOUBLE METAL I / C STANDARD CELL LIBRARY	**lrbf202** LOGIC_REFERENCE_CELL	DATE: Mar 9,1990 VERSION: dlmV2.2

TERMINAL LOCATION (In Lambda)

GRID	PIN_NAME	Y_LOC	CLASS
1	FeedThru	----	----
2	FeedThru	----	----
3	FeedThru	----	----
4	FeedThru	----	----
5	one	48	0
6	zero	32	0

LOGIC EQUATIONS: ZERO = 0
ONE = 1

Truth Table Not Applicable