

CONFIGURATION SETS FOR CSX-LITE

State	Configuration Set
s_0	$\text{Prog} \rightarrow \bullet \{ \text{Stmts} \} \text{Eof}$
s_1	$\text{Prog} \rightarrow \{ \bullet \text{Stmts} \} \text{Eof}$ $\text{Stmts} \rightarrow \bullet \text{Stmt Stmts}$ $\text{Stmts} \rightarrow \lambda \bullet$ $\text{Stmt} \rightarrow \bullet \text{id} = \text{Expr} ;$ $\text{Stmt} \rightarrow \bullet \text{if} (\text{Expr}) \text{Stmt}$
s_2	$\text{Prog} \rightarrow \{ \text{Stmts} \bullet \} \text{Eof}$
s_3	$\text{Stmts} \rightarrow \text{Stmt} \bullet \text{Stmts}$ $\text{Stmts} \rightarrow \bullet \text{Stmt Stmts}$ $\text{Stmts} \rightarrow \lambda \bullet$ $\text{Stmt} \rightarrow \bullet \text{id} = \text{Expr} ;$ $\text{Stmt} \rightarrow \bullet \text{if} (\text{Expr}) \text{Stmt}$
s_4	$\text{Stmt} \rightarrow \text{id} \bullet = \text{Expr} ;$
s_5	$\text{Stmt} \rightarrow \text{if} \bullet (\text{Expr}) \text{Stmt}$

State	Configuration Set
s_6	$\text{Prog} \rightarrow \{ \text{Stmts} \} \bullet \text{Eof}$
s_7	$\text{Stmts} \rightarrow \text{Stmt Stmts} \bullet$
s_8	$\text{Stmt} \rightarrow \text{id} = \bullet \text{Expr} ;$ $\text{Expr} \rightarrow \bullet \text{Expr} + \text{id}$ $\text{Expr} \rightarrow \bullet \text{Expr} - \text{id}$ $\text{Expr} \rightarrow \bullet \text{id}$
s_9	$\text{Stmt} \rightarrow \text{if} (\bullet \text{Expr}) \text{Stmt}$ $\text{Expr} \rightarrow \bullet \text{Expr} + \text{id}$ $\text{Expr} \rightarrow \bullet \text{Expr} - \text{id}$ $\text{Expr} \rightarrow \bullet \text{id}$
s_{10}	$\text{Prog} \rightarrow \{ \text{Stmts} \} \text{Eof} \bullet$
s_{11}	$\text{Stmt} \rightarrow \text{id} = \text{Expr} \bullet ;$ $\text{Expr} \rightarrow \text{Expr} \bullet + \text{id}$ $\text{Expr} \rightarrow \text{Expr} \bullet - \text{id}$
s_{12}	$\text{Expr} \rightarrow \text{id} \bullet$
s_{13}	$\text{Stmt} \rightarrow \text{if} (\text{Expr} \bullet) \text{Stmt}$ $\text{Expr} \rightarrow \text{Expr} \bullet + \text{id}$ $\text{Expr} \rightarrow \text{Expr} \bullet - \text{id}$

State	Configuration Set
s_{14}	$\text{Stmt} \rightarrow \text{id} = \text{Expr} ; \bullet$
s_{15}	$\text{Expr} \rightarrow \text{Expr} + \bullet \text{id}$
s_{16}	$\text{Expr} \rightarrow \text{Expr} - \bullet \text{id}$
s_{17}	$\text{Stmt} \rightarrow \text{if} (\text{Expr}) \bullet \text{Stmt}$ $\text{Stmt} \rightarrow \bullet \text{id} = \text{Expr} ;$ $\text{Stmt} \rightarrow \bullet \text{if} (\text{Expr}) \text{Stmt}$
s_{18}	$\text{Expr} \rightarrow \text{Expr} + \text{id} \bullet$
s_{19}	$\text{Expr} \rightarrow \text{Expr} - \text{id} \bullet$
s_{20}	$\text{Stmt} \rightarrow \text{if} (\text{Expr}) \text{Stmt} \bullet$

PARSER ACTION TABLE

We will table possible parser actions based on the current state (configuration set) and token.

Given configuration set C and input token T four actions are possible:

- Reduce i: The i-th production has been matched.
- Shift: Match the current token.
- Accept: Parse is correct and complete.
- Error: A syntax error has been discovered.

We will let $A[C][T]$ represent the possible parser actions given configuration set C and input token T .

$A[C][T] =$
 {Reduce i | i -th production is $A \rightarrow \alpha$
 and $A \rightarrow \alpha \bullet$ is in C
 and T in $\text{Follow}(A)$ }
 U (If $(B \rightarrow \beta \bullet T \gamma)$ is in C)
 {Shift} else ϕ)

This rule simply collects all the actions that a parser might do given C and T .

But we want parser actions to be unique so we require that the parser action always be *unique* for any C and T .

If the parser action isn't unique, then we have a shift/reduce error or reduce/reduce error. The grammar is then rejected as unparsable.

If parser actions are always unique then we will consider a shift of EOF to be an accept action.

An empty (or undefined) action for C and T will signify that token T is illegal given configuration set C .

A syntax error will be signaled.

LALR PARSER DRIVER

Given the GoTo and parser action tables, a Shift/Reduce (LALR) parser is fairly simple:

```
void LALRDriver(){
  Push(S0);
  while(true){
    //Let S = Top state on parse stack
    //Let CT = current token to match
    switch (A[S][CT]) {
      case error:
        SyntaxError(CT);return;
      case accept:
        return;
      case shift:
        push(GoTo[S][CT]);
        CT= Scanner();
        break;
      case reduce i:
        //Let prod i = A→Y1...Ym
        pop m states;
        //Let S' = new top state
        push(GoTo[S'][A]);
        break;
    } } }
```

ACTION TABLE for CSX-LITE

	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20		
{	S																						
}	R3	S	R3					R2							R4							R5	
if	S	S													R4		S					R5	
(S																		
)													R8	S							R6	R7	
id	S	S						S	S						R4	S	S	S					
=					S																		
+												S	R8	S							R6	R7	
-												S	R8	S							R6	R7	
;												S	R8								R6	R7	R5
eof							A																

GoTo Table for CSX-Lite

	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
{	1																				
}		6																			
if	5	5																	5		
(9																
)													17								
id	4	4						12	12										18	19	4
=					8																
+												15	15								
-												16	16								
;												14									
eof							10														
stmts	2	7																			
stmt	3	3																			
expr										11	13										

EXAMPLE of LALR(1) PARSING

We'll again parse

{ a = b + c; } Eof

We start by pushing state 0 on the parse stack.

Parse Stack	Top State	Action	Remaining Input
0	Prog → •{ Stmt } Eof	Shift	{ a = b + c; } Eof
1 0	Prog → { • Stmt } Eof Stmts → • Stmt Stmt Stmts → λ • Stmt → • id = Expr ; Stmt → • if (Expr)	Shift	a = b + c; } Eof
4 1 0	Stmt → id • = Expr ;		= b + c; } Eof
8 4 1 0	Stmt → id = • Expr ; Expr → • Expr + id Expr → • Expr - id Expr → • id	Shift	b + c; } Eof

Parse Stack	Top State	Action	Remaining Input
12 8 4 1 0	Expr → id •	Reduce 8	+ c; } Eof
11 8 4 1 0	Stmt → id = Expr • ; Expr → Expr • + id Expr → Expr • - id	Shift	+ c; } Eof
15 11 8 4 1 0	Expr → Expr + • id	Shift	c; } Eof

Parse Stack	Top State	Action	Remaining Input
18 15 11 8 4 1 0	Expr → Expr + id •	Reduce 6	; } Eof
11 8 4 1 0	Stmt → id = Expr • ; Expr → Expr • + id Expr → Expr • - id	Shift	; } Eof
14 11 8 4 1 0	Stmt → id = Expr ; •	Reduce 4	} Eof

Parse Stack	Top State	Action	Remaining Input
3 1 0	$Stmts \rightarrow Stmt \cdot Stmts$ $Stmts \rightarrow \cdot Stmt Stmts$ $Stmts \rightarrow \lambda \cdot$ $Stmt \rightarrow \cdot id = Expr ;$ $Stmt \rightarrow \cdot if (Expr)$ $Stmt$	Reduce 3	} Eof
7 3 1 0	$Stmts \rightarrow Stmt Stmts \cdot$	Reduce 2	} Eof
2 1 0	$Prog \rightarrow \{ Stmts \} \cdot Eof$	Shift	} Eof
6 2 1 0	$Prog \rightarrow \{ Stmts \} \cdot Eof$	Accept	Eof

ERROR DETECTION IN LALR PARSERS

In bottom-up, LALR parsers syntax errors are discovered when a blank (error) entry is fetched from the parser action table.

Let's again trace how the following illegal CSX-lite program is parsed:

```
{ b + c = a; } Eof
```

Parse Stack	Top State	Action	Remaining Input
0	$Prog \rightarrow \cdot \{ Stmts \} Eof$	Shift	{ b + c = a; } Eof
1 0	$Prog \rightarrow \{ \cdot Stmts \} Eof$ $Stmts \rightarrow \cdot Stmt Stmts$ $Stmts \rightarrow \lambda \cdot$ $Stmt \rightarrow \cdot id = Expr ;$ $Stmt \rightarrow \cdot if (Expr)$	Shift	b + c = a; } Eof
4 1 0	$Stmt \rightarrow id \cdot = Expr ;$	Error (blank)	+ c = a; } Eof

LALR is MORE POWERFUL

Essentially all LL(1) grammars are LALR(1) plus many more. Grammar constructs that confuse LL(1) are readily handled.

- Common prefixes are no problem. Since sets of configurations are tracked, more than one prefix can be followed. For example, in

```

Stmt → id = Expr ;
Stmt → id ( Args ) ;
after we match an id we have

```

```

Stmt → id · = Expr ;
Stmt → id · ( Args ) ;
The next token will tell us which
production to use.

```

- Left recursion is also not a problem. Since sets of configurations are tracked, we can follow a left-recursive production *and* all others it might use. For example, in

Expr → • **Expr** + id

Expr → • id

we can first match an **id**:

Expr → id •

Then the **Expr** is recognized:

Expr → **Expr** • + id

The left-recursion is handled!

- But ambiguity will still block construction of an LALR parser. Some shift/reduce or reduce/reduce conflict must appear. (Since two or more distinct parses are possible for some input). Consider our original productions for if-then and if-then-else statements:

Stmt → if (**Expr**) **Stmt** •

Stmt → if (**Expr**) **Stmt** • else **Stmt**

Since **else** can follow **Stmt**, we have an unresolvable shift/reduce conflict.

GRAMMAR ENGINEERING

Though LALR grammars are very general and inclusive, sometimes a reasonable set of productions is rejected due to shift/reduce or reduce/reduce conflicts.

In such cases, the grammar may need to be “engineered” to allow the parser to operate.

A good example of this is the definition of **MemberDecls** in CSX. A straightforward definition is

MemberDecls → **FieldDecls** **MethodDecls**

FieldDecls → **FieldDecl** **FieldDecls**

FieldDecls → λ

MethodDecls → **MethodDecl** **MethodDecls**

MethodDecls → λ

FieldDecl → int id ;

MethodDecl → int id () ; **Body**

When we predict **MemberDecls** we get:

MemberDecls → • **FieldDecls** **MethodDecls**

FieldDecls → • **FieldDecl** **FieldDecls**

FieldDecls → λ •

FieldDecl → • int id ;

Now **int** follows **FieldDecls** since **MethodDecls** ⇒⁺ **int** ...

Thus an unresolvable shift/reduce conflict exists.

The problem is that **int** is derivable from both **FieldDecls** and **MethodDecls**, so when we see an **int**, we can't tell which way to parse it (and **FieldDecls** → λ requires we make an immediate decision!).

If we rewrite the grammar so that we can delay deciding from where the int was generated, a valid LALR parser can be built:

```
MemberDecls → FieldDecl MemberDecls
MemberDecls → MethodDecls
MethodDecls → MethodDecl MethodDecls
MethodDecls → λ
FieldDecl → int id ;
MethodDecl → int id ( ) ; Body
```

When **MemberDecls** is predicted we have

```
MemberDecls → • FieldDecl MemberDecls
MemberDecls → • MethodDecls
MethodDecls → •MethodDecl MethodDecls
MethodDecls → λ •
FieldDecl → • int id ;
MethodDecl → • int id ( ) ; Body
```

Now **Follow(MethodDecls) = Follow(MemberDecls) = "}"**, so we have no shift/reduce conflict. After **int id** is matched, the next token (a ";" or a "(") will tell us whether a **FieldDecl** or a **MethodDecl** is being matched.

PROPERTIES OF LL AND LALR PARSERS

- Each prediction or reduce action is *guaranteed* correct. Hence the entire parse (built from LL predictions or LALR reductions) must be correct.

This follows from the fact that LL parsers allow only one valid prediction per step. Similarly, an LALR parser never skips a reduction if it is consistent with the current token (and *all* possible reductions are tracked).

- LL and LALR parsers detect a syntax error as soon as the first invalid token is seen.

Neither parser can match an invalid program prefix. If a token is matched it *must be* part of a valid program prefix. In fact, the prediction made or the stacked configuration sets *show* a possible derivation of the token accepted so far.

- All LL and LALR grammars are unambiguous.

LL predictions are always unique and LALR shift/reduce or reduce/reduce conflicts are disallowed. Hence only one valid derivation of any token sequence is possible.

- All LL and LALR parsers require only linear time and space (in terms of the number of tokens parsed).

The parsers do only fixed work per node of the concrete parse tree, and the size of this tree is linear in terms of the number of leaves in it (even with λ -productions included!).