

```
e1000_config_dsp_after_link_change(...) { ...
    if (hw->ffe_cfg_state == e1000_ffe_cfg_active) {
        ret_val = e1000_read_phy_reg(hw, 0x2F5B, &saved);
        if (ret_val) return ret_val;
        ret_val = e1000_write_phy_reg(hw, 0x2F5B, 0x0003);
        if (ret_val) return ret_val;
        mdelay(20);
        ret_val = e1000_write_phy_reg(hw, ...);
        if (ret_val) return ret_val;
        ret_val = e1000_write_phy_reg(hw, ...);
        if (ret_val) return ret_val;
        ret_val = e1000_write_phy_reg(hw, ...);
        if (ret_val) return ret_val;
        mdelay(20);
        ret_val = e1000_write_phy_reg(hw, ...);
        if (ret_val) return ret_val;
        hw->ffe_config_state = e1000_ffe_config_enabled;
    } ...
}
```