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SaiSuresh KrishnaKumaran

Objective

Seeking an Internship position in Computer Sciences for Summer 2005.

Areas of Interest

Computer Architecture, Operating Systems and Database Systems.

Education

August 2004 - present UW-Madison Madison, USA
MS (Computer Sciences)

2000 – 2004 Anna University Chennai, India
BE (Computer Science & Engineering)
GPA 9.542/10
Class Rank – 1 (out of 120 students)

Experience

TA (Teaching Assistant) for CS367 in Fall 2004 – Introduction to Data Structures

Publication

Saisuresh Krishnakumaran, Sai Arunachalam, **'Towards economic Trace Caches-a profile based approach',** Poster session of the *10th International Conference on High Performance Computing 2003*, Hyderabad, India.

Achievements

- My Team (Team Q) was placed **SECOND** among 187 teams in the 2004 **ACM North Central North America Programming Contest**.
- Department topper in Anna University.
- Tamil Nadu Engineering Admission: Ranked 3rd in the state of Tamil Nadu, India. (100,000 students appeared for the Tamil Nadu Professional Courses Entrance Examination)

Awards Received

- '**Late Thiru A. Muralitharan Endowment Prize**', a **Gold medal** by College of Engineering Guindy, Anna University for securing the highest marks in the second year of my undergraduate study.
- '**1951 Alumni Golden Jubilee Endowment Prize**' by the Alumni Association of Anna University for maintaining the highest CGPA.
- '**Aravind Mehta Memorial Award**' by the Alumni Association of Anna University for securing the highest marks in Mathematics III course in the college.

Computer Skills	*Operating systems*: LINUX, UNIX, Windows. *Programming languages*: C, C++, JAVA, Intel 8086 Assembly *Tools*: Lex, Yacc *Simulators*: SimpleScalar (Architecture Simulator) *Others*: SQL, VHDL, Handel-C, Shell scripts
Courses Taken @ UW-Madison	CS752 – Advanced Computer Architecture. CS764 – Topics in Database Management Systems

Projects

- *A Soft Core Processor for Parameterized HPL-PD Architecture.* It is a vehicle to investigate processor architectures having significant parallelism. It is a reliable model to analyze the performance of custom architectures and would facilitate in decreasing the time for hardware realization.
- *Re-configurable Architectural Kit (RAK).* RAK (implemented in VHDL) is a platform to study and analyze the performance of various static processor configurations.
- Implementation of 'Dynamic Instruction Reuse' using SimpleScalar.
- A 'C' compiler using LEX and YACC.
- *Disk access optimization using 'deferred copy' and disk block sharing in UNIX file system.* Added fields in the Inode and disk block structures of the UNIX file system. These fields were manipulated by new system calls in order optimize the disk block accesses.
- *Implementation of 'A mail server and a messenger' designed using Rational Rose.*
- *Device Driver for a virtual CD drive in Linux environment.*
- *Simulation of the Control Logic for an Automatic Teller Machine using VHDL.*
- *Automatic Traffic Controller – Hardware Project.*
- *Simulation of Task Scheduling and Interrupt Processing by Microprocessors.*

Extra/Co - curricular activities

- I was an active member of the SIGARCH, the computer architecture research group at Anna University, India.
- I have won a number of certificates and prizes in Oratorical, Essay Writing and Quiz Competitions.

Responsibilities

- I had been an active member and organizer of 'Abacus', the technical symposium of DCSE, Anna University, India.
- NSS (National Service Scheme) volunteer. Attended a 15-day camp at a village near Chennai, India during June 2001. Also played an active role in organizing a free medical camp for the poor villagers there.

Special Interests

Meditation, Cricket and Tamil Poems