Note: Tutorial assumes you are using bash

Steps to Setup ModelSim

1. ssh <username>@royal-01.cs.wisc.edu

2. Add the below line to ~/.bashrc file

export MGC_HOME=/s/mentor-2018/@sys/V10.0BSXE/MGC_HOME.ixl

export SDD_HOME=/s/mentor-2018/@sys/V10.0BSXE/SDD_HOME

export PATH=\$PATH:/s/mentor-2018/@sys/bin:/s/mentor-2018/@sys/bin.pclinux:/s/mentor2018/@sys/V10.0BSXE/MGC_HOME.ixl/bin:/s/mentor-2018/@sys/modelsim_dlx/bin

export MGLS_LICENSE_FILE=/s/mentor-2018/etc/cust/mgls/mgc.licenses

3. source ~/.bashrc

4. Create a directory called CS/ECE552 in your workarea and run vsim &

5. To run your simulation, you will need to create a project.

Click on File -> New -> Project. You will see the window presented below. Choose a location for your new project and give it the name. Leave the other settings to their default. This just says that all code will be compiled into the library "work".

Library :					
Name	Туре	Path			
• work	Library	cs.wisc.edu/u/m/a/mahapatra/privat			
floatfixlib	Library	\$MODEL_TECH//floatfixlib			
ieee_env (empty)	Library	\$MODEL_TECH//ieee_env			
🗄 📶 infact	Library	SMODEL_TEQU/ International Andrews			
mgc_ams (empty)	Library	SMODEL W Create Project			
🗄 📶 mtiRnm	Library	SMODELProject Name			
esvvm	Library	SMODEL_			
sv_std	Library	SMODEL_			
UNDER VIN UX01v lib	Library	SMODEL Project Location			
· vhdlopt_lib	Library	SMODEL_ a/mahapatra/private/vsim_te Browse			
vital2000	Library	SMODEL_			
E ieee	Library	IODELDefault Library Name			
modelsim_lib	Library	SMODEL work			
std	Library	DEL []			
std_developerskit	Library	SMODELCopy Settings From			
synopsys	Library	SMODEL lsim dlx/modelsim.ini Browse.			
± venlog	Library	SMODEL_			
		Copy Library Mappings C Reference Library Mappings			

6. Click on Add Existing File as shown in the picture. Download test.v and test_tb.v from Canvas. Navigate to the location where you downloaded test.v and test_tb.v and add both of those to your project. Keep other settings at their default. Click OK when done.

NEN C	
Create New File Add Ex	cisting File
Create Simulation Create !	Vew Folder

7. You will need to compile the source files first. To do this, right click on test.v, click on Compile, then click on Compile All. You should see messages in the Console window appear in green that the compile was successful.

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Name	Statu: Type	Orde Modified			
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h] test_tb.v	Verilog	1 01/18/2019 03:11:44			



8. To start your simulation, click on Simulate in the Menu Bar, then click Start Simulation. This opens the Start Simulation Window. Click on the plus sign next to work, then click on the plus sign next to arith_tb. Make sure you select arith_tb and not arith as we want to simulate the design at the test bench level. Once arith_tb is highlighted, click OK.

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Transcript	
Compile of test.v was successful.	
Compile of test_tb.v was successful.	
2 compiles, 0 failed with no errors.	
odelSim>	

9. Click on Simulate \rightarrow Run \rightarrow Run-All. You should see "MODELSIM SETUP SUCCESSFUL"



'SIM 4>

FAQs

Q1. For errors related to DISPLAY not set

- For Windows, download and install <u>Xming</u>. For X forwarding to work, you'll need to start Xming before connecting to the remote system with your SSH client (e.g., PuTTY).
- For Mac OS X 10.8 and later, download and install <u>XQuartz</u>. For X forwarding to work, you'll need to start XQuartz before making an SSH connection to the remote system. Once XQuartz launches, you can use X forwarding with SSH from the Mac OS X Terminal or from the <u>xterm</u> application in XQuartz.

The ssh command to log onto CS machines will be: ssh -Y <user_name>@best-linux.cae.wisc.edu To launch Modelsim you simply need to type "vsim".